

# SGM8545 1.1MHz, 48µA, Rail-to-Rail I/O CMOS Operational Amplifier

## **PRODUCT DESCRIPTION**

The SGM8545 is a low cost, single rail-to-rail input and output voltage feedback amplifier. It has a wide input common mode voltage range and output voltage swing, and takes the minimum operating supply voltage down to 2.1V. The maximum recommended supply voltage is 5.5V. It is specified over the extended -40°C to +125°C temperature range.

The SGM8545 provides 1.1MHz bandwidth at a low current consumption of  $48\mu$ A. Very low input bias current of 0.5pA enables SGM8545 to be used for integrators, photodiode amplifiers, and piezoelectric sensors. Rail-to-rail input and output are useful to designers for buffering ASIC in single-supply systems.

Applications for this amplifier include safety monitoring, portable equipment, battery and power supply control, and signal conditioning and interfacing for transducers in very low power systems.

The SGM8545 is available in the Green SOT-23-5 package.

## **APPLICATIONS**

ASIC Input or Output Amplifier Sensor Interface Piezoelectric Transducer Amplifier Medical Instrumentation Mobile Communication Audio Output Portable Systems Smoke Detectors Mobile Telephone Notebook PC PCMCIA Cards Battery-Powered Equipment

## FEATURES

- Low Cost
- Rail-to-Rail Input and Output
  0.8mV Typical Vos
- Unity Gain Stable
- Gain-Bandwidth Product: 1.1MHz
- Very Low Input Bias Current: 0.5pA
- Supply Voltage Range: 2.1V to 5.5V
- Input Voltage Range: -0.1V to +5.6V with V<sub>s</sub> = 5.5V
- Low Supply Current: 48µA
- Available in Green SOT-23-5 Package

#### **PIN CONFIGURATION (TOP VIEW)**

#### SGM8545





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## **PACKAGE/ORDERING INFORMATION**

MODEL	ORDER NUMBER	PACKAGE DESCRIPTION	PACKAGE OPTION	MARKING INFORMATION	
SGM8545	SGM8545XN5/TR	SOT-23-5	Tape and Reel, 3000	8545	

## **ABSOLUTE MAXIMUM RATINGS**

Supply Voltage, $+V_S$ to $-V_S$	Operating Temperature Range40°C to +125°C Lead Temperature (Soldering 10sec)260°C
Junction Temperature	ESD Susceptibility HBM4000V MM400V

#### NOTE:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## CAUTION

This integrated circuit can be damaged by ESD if you don't pay attention to ESD protection. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

SGMICRO reserves the right to make any change in circuit design, specification or other related things if necessary without notice at any time. Please contact SGMICRO sales office to get the latest datasheet.



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# **ELECTRICAL CHARACTERISTICS**

(At V<sub>S</sub> = +5V, R<sub>L</sub> = 100k $\Omega$  connected to Vs/2, and V<sub>OUT</sub> = Vs/2, unless otherwise noted.)

			ТҮР	MIN/MAX OVER TEMPERATURE			
PARAMETER	SYMBOL	CONDITIONS	+25℃	+25℃	-40°C to +125°C	UNITS	MIN/MAX
INPUT CHARACTERISTICS							
Input Offset Voltage	Vos	$V_{CM} = V_S/2$	0.8	3.5		mV	MAX
Input Bias Current	Ι <sub>Β</sub>		0.5			pА	TYP
Input Offset Current	I <sub>os</sub>		0.5			pА	TYP
Input Common Mode Voltage Range	V <sub>CM</sub>	V <sub>S</sub> = 5.5V	- 0.1 to + 5.6			V	TYP
Common Mode Dejection Datio	CMRR	$V_{\rm S}$ = 5.5V, $V_{\rm CM}$ = -0.1V to +4V	88	88 71 70		٩D	N AIN I
Common Mode Rejection Ratio	CIVIKK	$V_{\rm S}$ = 5.5V, $V_{\rm CM}$ = -0.1V to +5.6V	76	60	58	dB	MIN
Onen Lean Valtage Cain		$R_{L} = 5k\Omega, V_{O} = +0.1V \text{ to } +4.9V$	100	100 74 72		٩D	N AIN I
Open-Loop Voltage Gain	A <sub>OL</sub>	$R_L$ = 100k $\Omega$ , $V_O$ = +0.035V to +4.965V	105	85	77	dB	MIN
Input Offset Voltage Drift	$\Delta V_{OS} / \Delta_T$		2.7			µV/°C	TYP
OUTPUT CHARACTERISTICS		·					
	V <sub>OH</sub>	R <sub>L</sub> = 100kΩ	4.997	4.980	4.970	V	MIN
Outrut Maltana Outra	V <sub>OL</sub>	R <sub>L</sub> = 100kΩ	5	20	30	mV	MAX
Output Voltage Swing	V <sub>OH</sub>	R <sub>L</sub> = 10kΩ	4.992	4.970	4.960	V	MIN
	V <sub>OL</sub>	R <sub>L</sub> = 10kΩ	8	30	40	mV	MAX
Output Ourpart	ISOURCE	$D = 400 \pm 1/2$	84	60	45	m (	MIN
Output Current	I <sub>SINK</sub>	$R_L = 10\Omega$ to $V_S/2$	75	60	45	mA	
POWER SUPPLY							
				2.1	2.5	V	MIN
Operating Voltage Range				5.5	5.5	V	MAX
Power Supply Rejection Ratio	PSRR	$V_{\rm S}$ = +2.5V to +5.5V, $V_{\rm CM}$ = +0.5V	86	70	68	dB	MIN
Quiescent Current	Ι <sub>Q</sub>		48	69	89	μA	MAX
DYNAMIC PERFORMANCE (CL = 100	) DpF)						
Gain-Bandwidth Product	GBP		1.1			MHz	TYP
Slew Rate	SR	G = +1, 2V Output Step	0.52			V/µs	TYP
Settling Time to 0.1%	ts	G = +1, 2V Output Step	5.3			μs	TYP
Overload Recovery Time		V <sub>IN</sub> ·Gain = V <sub>S</sub>	2.6			μs	TYP
NOISE PERFORMANCE		•					
Vallaga Naisa Dagaitu		f = 1kHz	27			nV/ <sub>√Hz</sub>	TYP
Voltage Noise Density	en	f = 10kHz	20			nV/ <sub>√Hz</sub>	TYP



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## **TYPICAL PERFORMANCE CHARACTERISTICS**





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## **TYPICAL PERFORMANCE CHARACTERISTICS**

At  $T_A$  = +25°C,  $V_S$  = +5V, and  $R_L$  = 100k $\Omega$  connected to  $V_S/2$ , unless otherwise noted.



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At  $T_A$  = +25°C,  $V_S$  = +5V, and  $R_L$  = 100k $\Omega$  connected to  $V_S/2$ , unless otherwise noted.





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## **APPLICATION NOTES**

#### **Driving Capacitive Loads**

The SGM8545 can directly drive 250pF in unity-gain without oscillation. The unity-gain follower (buffer) is the most sensitive configuration to capacitive loading. Direct capacitive loading reduces the phase margin of amplifiers and this results in ringing or even oscillation. Applications that require greater capacitive driving capability should use an isolation resistor between the output and the capacitive load like the circuit in Figure 1. The isolation resistor R<sub>ISO</sub> and the load capacitor C<sub>L</sub> form a zero to increase stability. The bigger the R<sub>ISO</sub> resistor value, the more stable V<sub>OUT</sub> will be. Note that this method results in a loss of gain accuracy because R<sub>ISO</sub> forms a voltage divider with the R<sub>LOAD</sub>.



Figure 1. Indirectly Driving Heavy Capacitive Load

An improved circuit is shown in Figure 2. It provides DC accuracy as well as AC stability.  $R_F$  provides the DC accuracy by connecting the inverting signal with the output.  $C_F$  and  $R_{Iso}$  serve to counteract the loss of phase margin by feeding the high frequency component of the output signal back to the amplifier's inverting input, thereby preserving phase margin in the overall feedback loop.



#### Figure 2. Indirectly Driving Heavy Capacitive Load with DC Accuracy

For non-buffer configuration, there are two other ways to increase the phase margin: (a) by increasing the amplifier's gain or (b) by placing a capacitor in parallel with the feedback resistor to counteract the parasitic capacitance associated with inverting node.

#### **Power-Supply Bypassing and Layout**

The SGM8545 operates from either a single +2.1V to +5.5V supply or dual ±1.05V to ±2.75V supplies. For single-supply operation, bypass the power supply +V<sub>S</sub> with a 0.1µF ceramic capacitor which should be placed close to the +V<sub>S</sub> pin. For dual-supply operation, both the +V<sub>S</sub> and the -V<sub>S</sub> supplies should be bypassed to ground with separate 0.1µF ceramic capacitors. 2.2µF tantalum capacitor can be added for better performance.



Figure 3. Amplifier with Bypass Capacitors



# **TYPICAL APPLICATION CIRCUITS**

#### **Differential Amplifier**

The circuit shown in Figure 4 performs the difference function. If the resistor ratios are equal to (R4/R3 = R2/R1), then  $V_{OUT}$  = (Vp - Vn) × R2/R1 + V<sub>REF</sub>.



Figure 4. Differential Amplifier

#### **Instrumentation Amplifier**

The circuit in Figure 5 performs the same function as that in Figure 4 but with a high input impedance.



Figure 5. Instrumentation Amplifier

#### Low Pass Active Filter

The low pass filter shown in Figure 6 has a DC gain of (-R2/R1) and the -3dB corner frequency is  $1/2\pi R_2 C$ . Make sure the filter bandwidth is within the bandwidth of the amplifier. The large values of feedback resistors can couple with parasitic capacitance and cause undesired effects such as ringing or oscillation in high-speed amplifiers. Keep resistor values as low as possible and consistent with output loading consideration.



Figure 6. Low Pass Active Filter



# PACKAGE OUTLINE DIMENSIONS

SOT-23-5





RECOMMENDED LAND PATTERN (Unit: mm)





Symbol	-	nsions imeters	Dimensions In Inches		
	MIN	MAX	MIN	MAX	
A	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
с	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
е	0.950 BSC		0.037 BSC		
e1	1.900 BSC		0.075 BSC		
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	



# TAPE AND REEL INFORMATION

#### **REEL DIMENSIONS**



NOTE: The picture is only for reference. Please make the object as the standard.

#### KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOT-23-5	7"	9.5	3.17	3.23	1.37	4.0	4.0	2.0	8.0	Q3



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#### **CARTON BOX DIMENSIONS**



NOTE: The picture is only for reference. Please make the object as the standard.

#### **KEY PARAMETER LIST OF CARTON BOX**

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7" (Option)	368	227	224	8
7"	442	410	224	18

