



# SGM8969-1/SGM8969-2

## 1.1mA, 50MHz, High Precision, Rail-to-Rail I/O, Low Noise, $G \geq 10$ Stable CMOS Operational Amplifiers

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### GENERAL DESCRIPTION

The SGM8969-1/2 are a family of single and dual rail-to-rail input and output operational amplifiers with 50MHz gain-bandwidth product and 20V/ $\mu$ s slew rate, while consuming only 1.1mA quiescent current per amplifier at 5.5V.

The SGM8969-1/2 feature a 240 $\mu$ V maximum input offset, and the devices are optimized for low voltage operation from 1.8V to 5.5V.

The single SGM8969-1 is available in a Green SOT-23-5 package. The dual SGM8969-2 is available in Green SOIC-8 and TDFN-3 $\times$ 3-8L packages. They are specified over the extended industrial temperature range (-40 $^{\circ}$ C to +125 $^{\circ}$ C).

### FEATURES

- Rail-to-Rail Input and Output
- Input Offset Voltage: 240 $\mu$ V (MAX)
- High Gain-Bandwidth Product: 50MHz
- High Slew Rate: 20V/ $\mu$ s
- Settling Time to 0.1% with 2V Step: 500ns
- Overload Recovery Time: 50ns
- Low Noise: 8nV/ $\sqrt{\text{Hz}}$  at 10kHz
- Gain 10 Stable
- Supply Voltage Range: 1.8V to 5.5V
- Input Voltage Range: -0.1V to 5.6V with  $V_S = 5.5V$
- Low Power:  
Supply Current: 1.1mA/Amplifier (TYP)
- -40 $^{\circ}$ C to +125 $^{\circ}$ C Operating Temperature Range
- Small Packaging:  
SGM8969-1 Available in a Green SOT-23-5 Package  
SGM8969-2 Available in Green SOIC-8 and  
TDFN-3 $\times$ 3-8L Packages

### APPLICATIONS

Sensor  
Audio  
Active Filter  
A/D Converter  
Communication  
Test Equipment  
Cellular and Cordless Phone  
Laptop and PDA  
Photodiode Amplification  
Battery-Powered Instrumentation

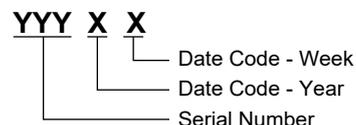
**PACKAGE/ORDERING INFORMATION**

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
SGM8969-1	SOT-23-5	-40°C to +125°C	SGM8969-1XN5G/TR	MB8XX	Tape and Reel, 3000
SGM8969-2	SOIC-8	-40°C to +125°C	SGM8969-2XS8G/TR	SGM 89692XS8 XXXXX	Tape and Reel, 4000
	TDFN-3×3-8L	-40°C to +125°C	SGM8969-2XTDB8G/TR	SGM 89692DB XXXXX	Tape and Reel, 4000

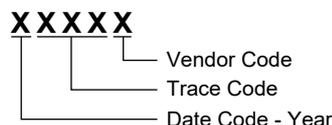
**MARKING INFORMATION**

NOTE: XX = Date Code. XXXXX = Date Code, Trace Code and Vendor Code.

**SOT-23-5**



**SOIC-8/TDFN-3×3-8L**



Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

**ABSOLUTE MAXIMUM RATINGS**

- Supply Voltage, +V<sub>S</sub> to -V<sub>S</sub>.....6V
- Input Common Mode Voltage Range  
 ..... (-V<sub>S</sub>) - 0.3V to (+V<sub>S</sub>) + 0.3V
- Junction Temperature .....+150°C
- Storage Temperature Range.....-65°C to +150°C
- Lead Temperature (Soldering, 10s) .....+260°C
- ESD Susceptibility
- HBM.....7000V
- CDM ..... 1000V

**ESD SENSITIVITY CAUTION**

This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

**RECOMMENDED OPERATING CONDITIONS**

- Operating Temperature Range .....-40°C to +125°C

**DISCLAIMER**

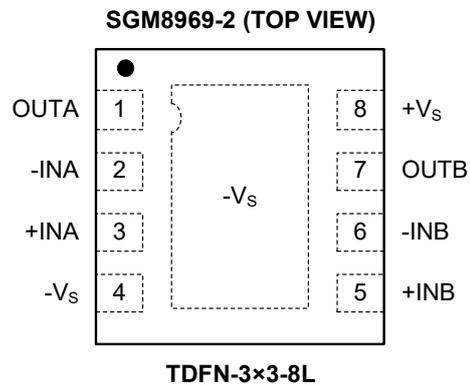
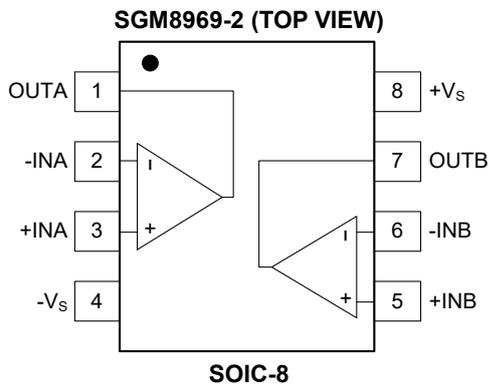
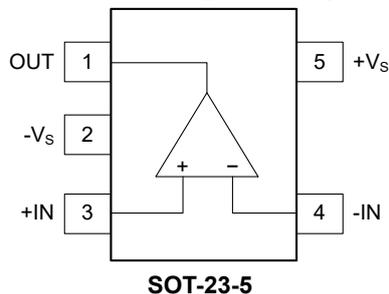
SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

**OVERSTRESS CAUTION**

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

## PIN CONFIGURATIONS

SGM8969-1 (TOP VIEW)



NOTE: For TDFN-3x3-8L package, connect exposed pad to -Vs.

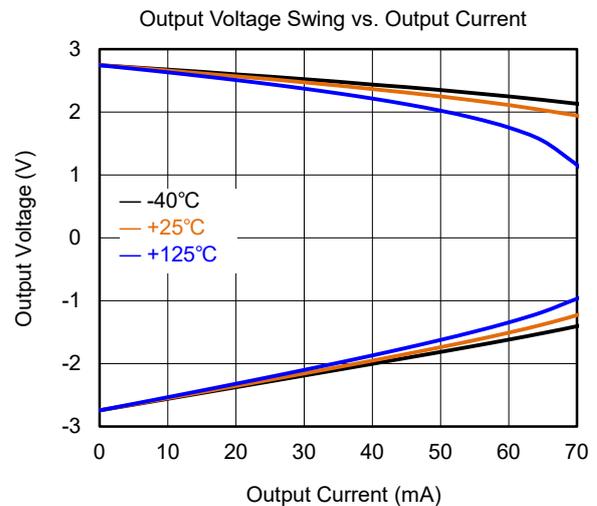
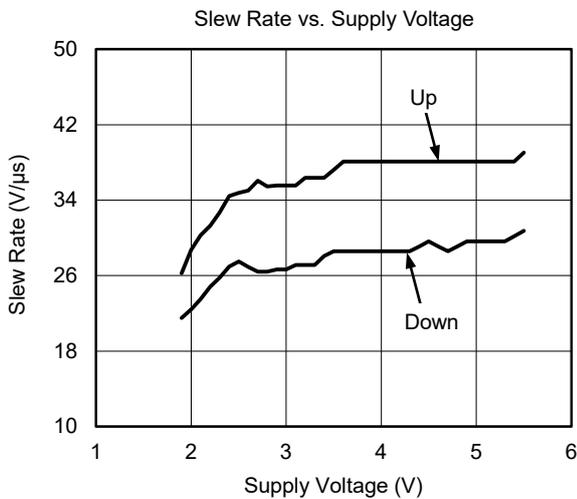
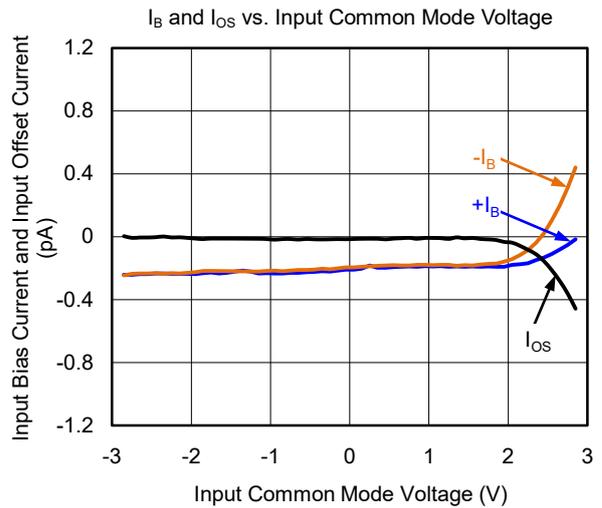
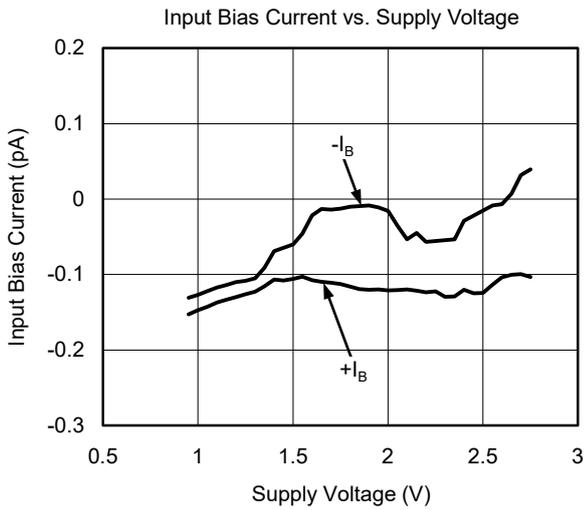
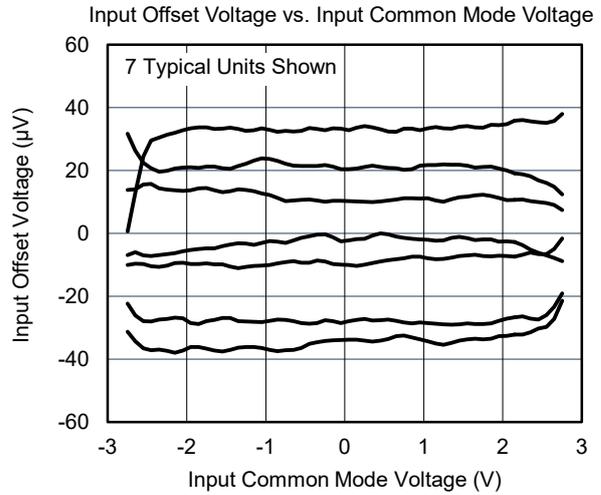
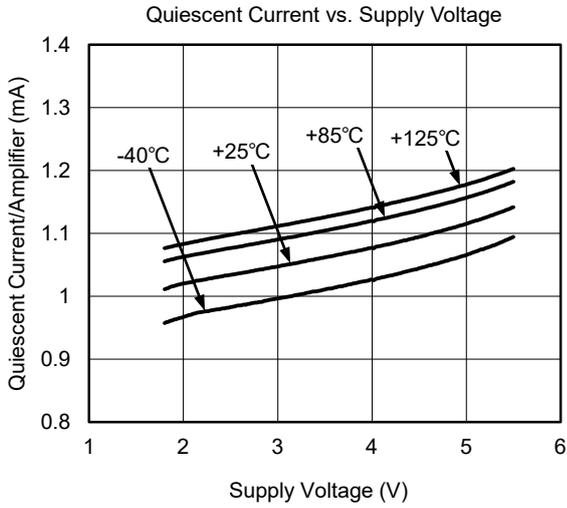
## ELECTRICAL CHARACTERISTICS

(At  $T_A = +25^\circ\text{C}$ ,  $V_S = 1.8\text{V}$  to  $5.5\text{V}$  or  $\pm 0.9\text{V}$  to  $\pm 2.75\text{V}$ ,  $V_{CM} = V_S/2$  and  $R_L = 10\text{k}\Omega$  connected to  $V_S/2$ , Full =  $-40^\circ\text{C}$  to  $+125^\circ\text{C}$ , unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
<b>Input Characteristics</b>							
Input Offset Voltage	$V_{OS}$		+25°C		50	240	$\mu\text{V}$
			Full			750	
Input Bias Current	$I_B$		+25°C		6	120	pA
			Full			4000	
Input Offset Current	$I_{OS}$		+25°C		6	120	pA
			Full			1000	
Input Common Mode Voltage Range	$V_{CM}$		Full	$(-V_S) - 0.1$		$(+V_S) + 0.1$	V
Common Mode Rejection Ratio	CMRR	$V_S = 5.5\text{V}$ , $V_{CM} = -0.1\text{V}$ to $5.6\text{V}$	+25°C	84	102		dB
			Full	81			
		$V_S = 1.8\text{V}$ , $V_{CM} = -0.1\text{V}$ to $1.9\text{V}$	+25°C	76	95		
			Full	73			
Open-Loop Voltage Gain	$A_{OL}$	$V_S = \pm 0.9\text{V}$ , $R_L = 1\text{k}\Omega$ , $(-V_S) + 0.25\text{V} < V_{OUT} < (+V_S) - 0.25\text{V}$	+25°C	91	117		dB
			Full	88			
		$V_S = \pm 2.75\text{V}$ , $R_L = 1\text{k}\Omega$ , $(-V_S) + 0.25\text{V} < V_{OUT} < (+V_S) - 0.25\text{V}$	+25°C	102	128		
			Full	99			
		$V_S = \pm 0.9\text{V}$ , $R_L = 10\text{k}\Omega$ , $(-V_S) + 0.15\text{V} < V_{OUT} < (+V_S) - 0.15\text{V}$	+25°C	94	118		
			Full	91			
		$V_S = \pm 2.75\text{V}$ , $R_L = 10\text{k}\Omega$ , $(-V_S) + 0.15\text{V} < V_{OUT} < (+V_S) - 0.15\text{V}$	+25°C	102	127		
			Full	99			
<b>Output Characteristics</b>							
Output Voltage Swing from Rail	$V_{OUT}$	$V_S = 5.5\text{V}$ , $R_L = 1\text{k}\Omega$	+25°C		60	75	mV
			Full			80	
		$V_S = 5.5\text{V}$ , $R_L = 10\text{k}\Omega$	+25°C		12	18	
			Full			20	
Output Current ( $I_{OUT}$ )	$I_{OUT}$	$V_S = 5.5\text{V}$	+25°C	30	50		mA
			Full	12			
<b>Power Supply</b>							
Operating Voltage Range	$V_S$		Full	1.8		5.5	V
Power Supply Rejection Ratio	PSRR	$V_S = 1.8\text{V}$ to $5.5\text{V}$ , $V_{CM} = (-V_S) + 0.5\text{V}$	+25°C	89	106		dB
			Full	86			
Quiescent Current/Amplifier	$I_Q$	$I_{OUT} = 0$	+25°C		1.1	1.55	mA
			Full			1.6	
<b>Dynamic Performance</b>							
Gain-Bandwidth Product	GBP	$V_S = 5\text{V}$	+25°C		50		MHz
Phase Margin	$\phi_O$	$V_S = 5\text{V}$	+25°C		60		°
Slew Rate	SR	$V_S = 5\text{V}$ , $G = +10$ , 2V output step	+25°C		20		V/ $\mu\text{s}$
Settling Time to 0.1%	$t_S$	$V_S = 5\text{V}$ , $G = +10$ , 2V output step	+25°C		500		ns
Overload Recovery Time		$V_S = 5\text{V}$ , $V_{IN} \times G = V_S$	+25°C		50		ns
<b>Noise Performance</b>							
Input Voltage Noise Density	$e_n$	$f = 1\text{kHz}$	+25°C		20		$\text{nV}/\sqrt{\text{Hz}}$
		$f = 10\text{kHz}$	+25°C		8		

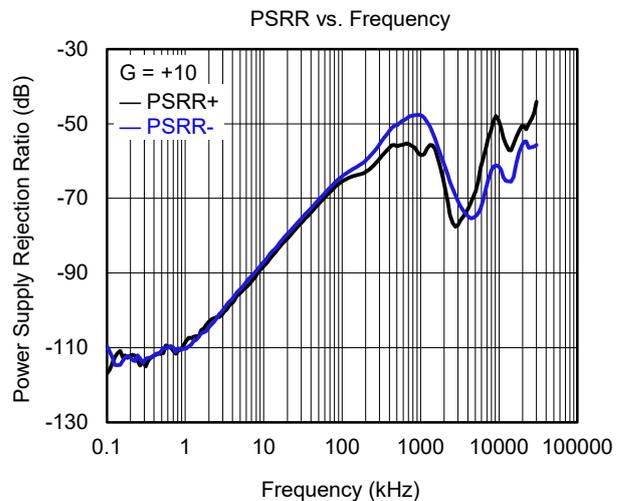
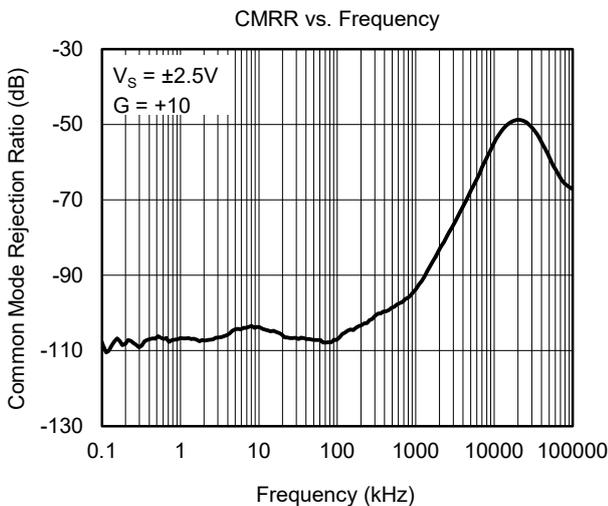
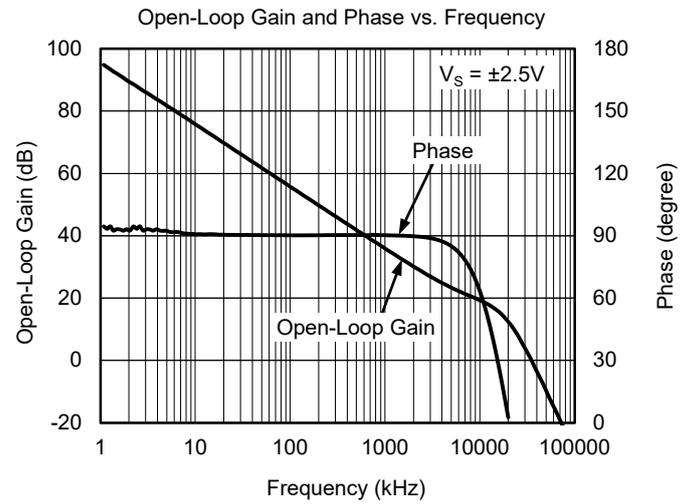
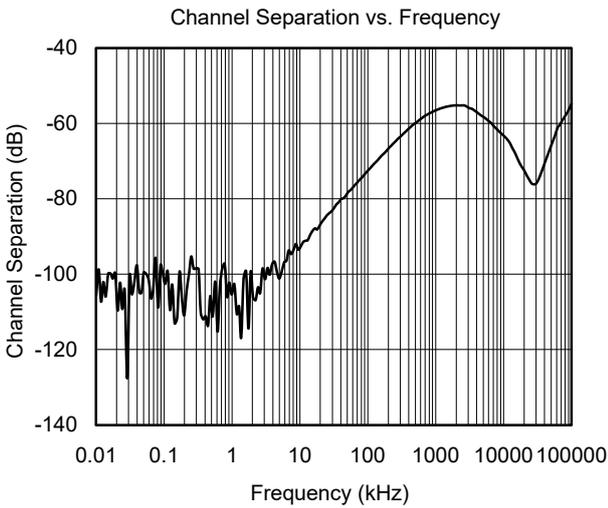
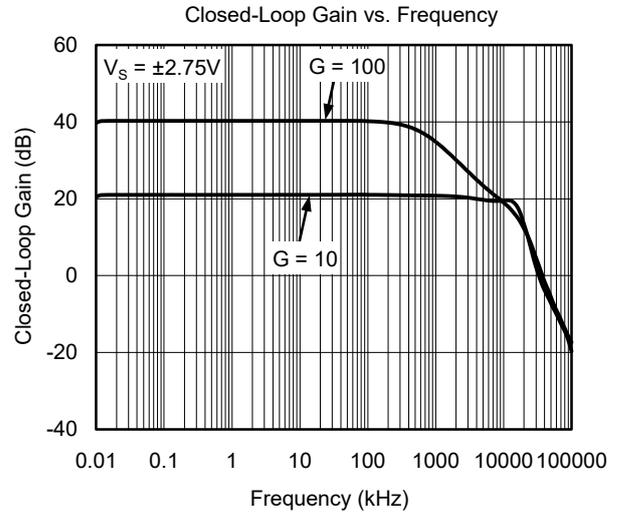
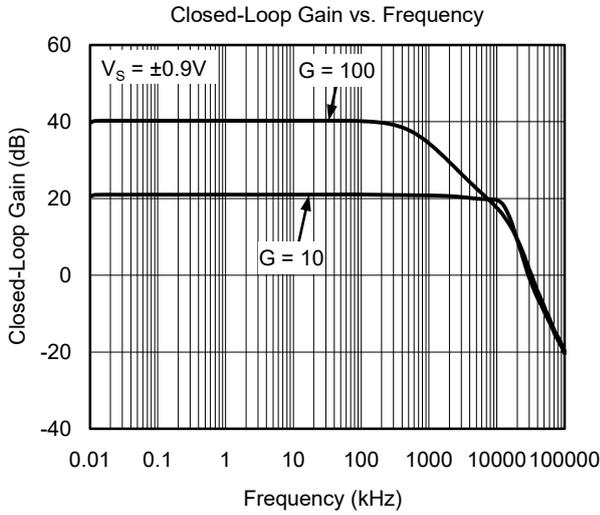
**TYPICAL PERFORMANCE CHARACTERISTICS**

At  $T_A = +25^\circ\text{C}$ ,  $V_S = \pm 2.75\text{V}$  and  $R_L = 10\text{k}\Omega$ , unless otherwise noted.



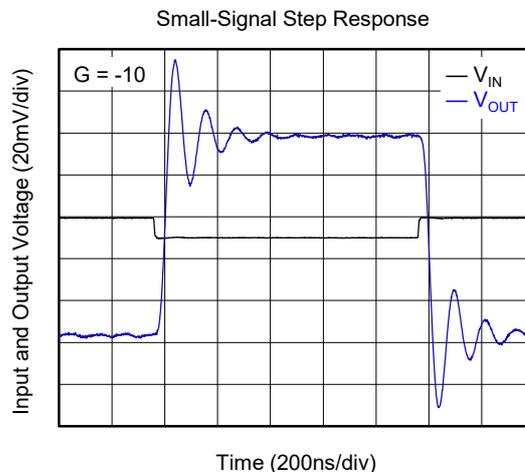
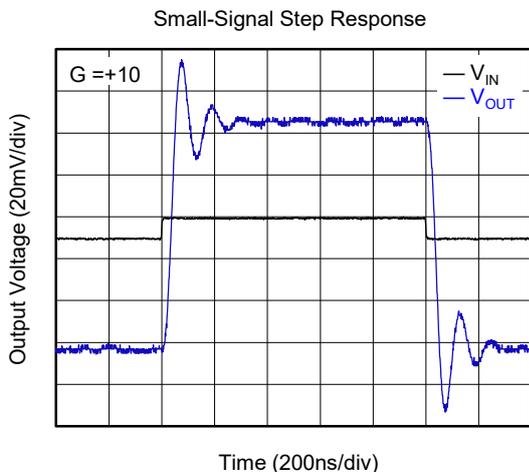
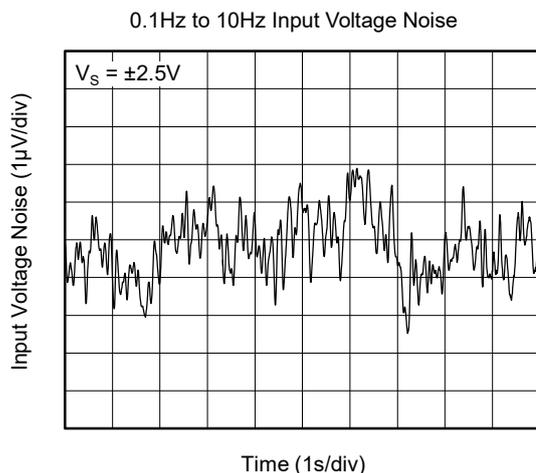
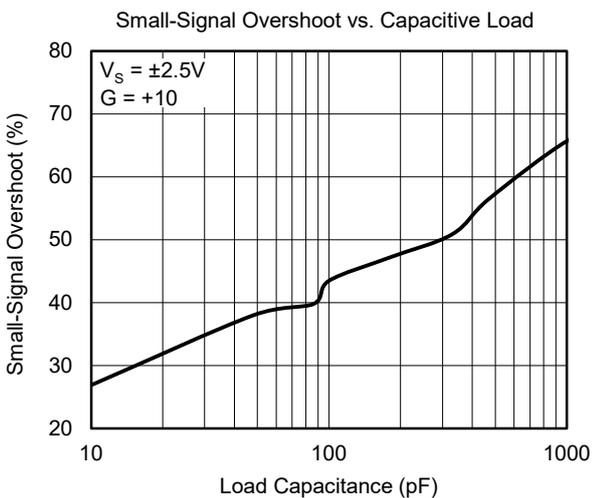
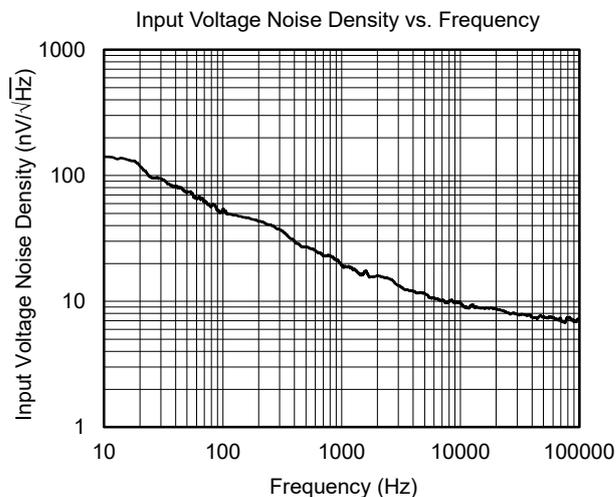
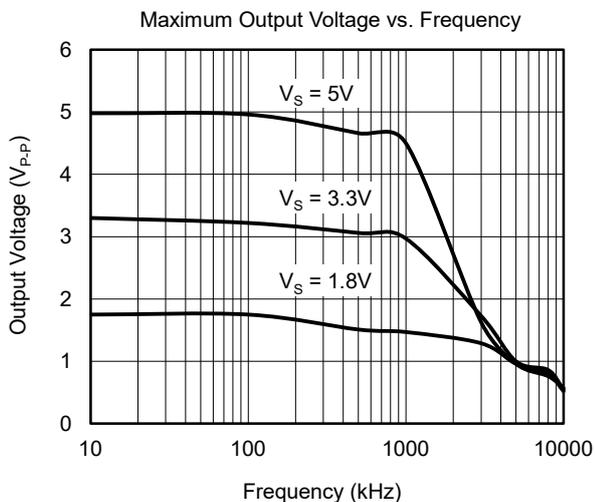
**TYPICAL PERFORMANCE CHARACTERISTICS (continued)**

At  $T_A = +25^\circ\text{C}$ ,  $V_S = \pm 2.75\text{V}$  and  $R_L = 10\text{k}\Omega$ , unless otherwise noted.



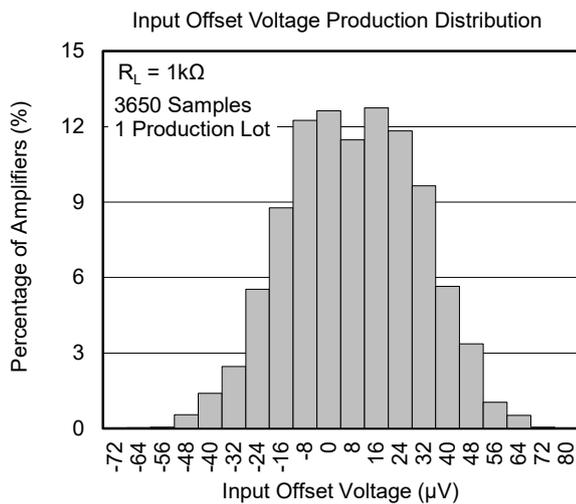
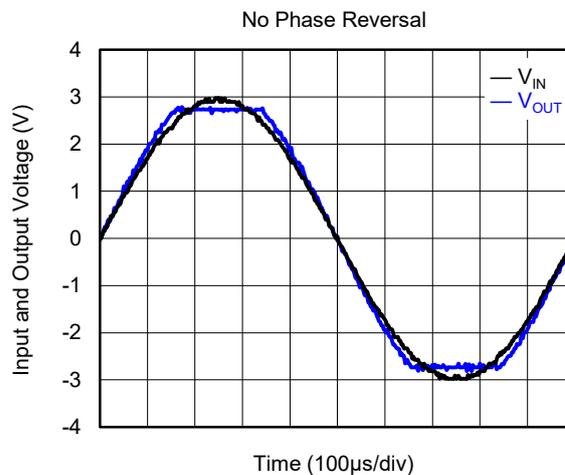
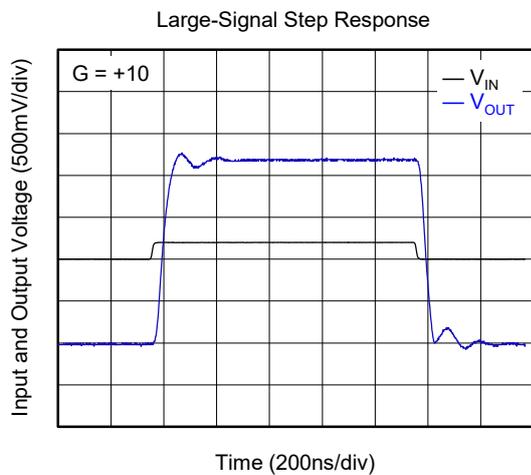
## TYPICAL PERFORMANCE CHARACTERISTICS (continued)

At  $T_A = +25^\circ\text{C}$ ,  $V_S = \pm 2.75\text{V}$  and  $R_L = 10\text{k}\Omega$ , unless otherwise noted.



**TYPICAL PERFORMANCE CHARACTERISTICS (continued)**

At  $T_A = +25^\circ\text{C}$ ,  $V_S = \pm 2.75\text{V}$  and  $R_L = 10\text{k}\Omega$ , unless otherwise noted.



## APPLICATION NOTES

### Rail-to-Rail Input

The input common mode voltage range of the SGM8969-1/2 extends 100mV beyond the supply rails for the full supply voltage range of 1.8V to 5.5V. Diodes between the inputs and the supply rails keep the input voltage from exceeding the rails.

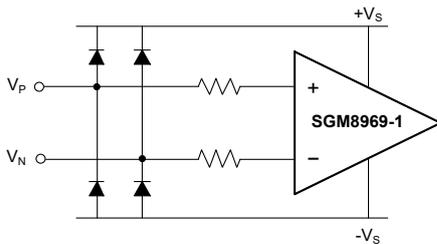


Figure 1. Equivalent Input Circuit

### Input Protection

The SGM8969-1/2 family incorporates internal ESD protection circuits on all pins. For input and output pins, this protection primarily consists of current-steering diodes connected between the input and power supply pins. Therefore, as well as keeping the input voltage below the maximum rating, it is also important to limit the input current to less than 10mA. Figure 2 shows how a series input resistor can be added to the driven input to limit the input current. The added resistor contributes thermal noise at the amplifier input and the value must be kept to a minimum in noise-sensitive applications.

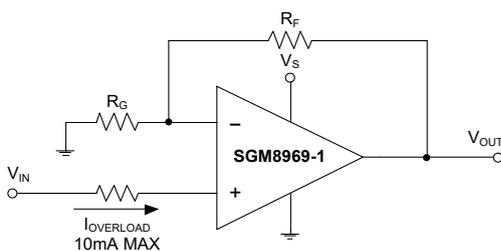


Figure 2. Input Current Protection

### Rail-to-Rail Output

The minimum output voltage will be within millivolts of ground for single-supply operation where the load is referenced to ground ( $-V_s$ ). With a 5.5V supply and the 10kΩ load tied to ground, the typical output swings from 0.012V to 5.488V.

### Driving Capacitive Loads

The SGM8969-1/2 are gain 10 stable for capacitive load up to 470pF. Applications that require greater capacitive drive capability should use an isolation resistor between the output and the capacitive load (Figure 3). Note that this alternative results in a loss of gain accuracy because  $R_{ISO}$  forms a voltage divider with the  $R_{LOAD}$ .

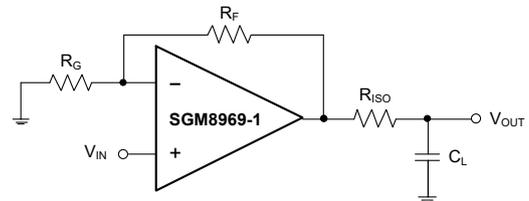


Figure 3. Using Isolation Resistor to Improve Stability when Driving Heavy Capacitive Load

### Power Supply Bypassing and Layout

Power supply pins are actually inputs to the amplifiers. Care must be taken to provide the amplifiers with a clean, low noise DC voltage source.

Power supply bypassing is employed to provide a low impedance path to ground for noise and undesired signals at all frequencies. This cannot be achieved with a single capacitor type; but with a variety of capacitors in parallel, the bandwidth of power supply bypassing can be greatly extended. The bypass capacitors have two functions:

1. Provide a low impedance path for noise and undesired signals from the supply pins to ground.
2. Provide local stored charge for fast switching conditions and minimize the voltage drop at the supply pins during transients. This is typically achieved with large electrolytic capacitors.

**APPLICATION NOTES (continued)**

Good quality ceramic chip capacitors should be used and always kept as close as possible to the amplifier package. A parallel combination of a 0.1µF ceramic and a 10µF electrolytic covers a wide range of rejection for unwanted noise. The 10µF capacitor is less critical for high frequency bypassing, and in most cases, one per supply line is sufficient. The values of capacitors are circuit-dependent and should be determined by the system’s requirements.

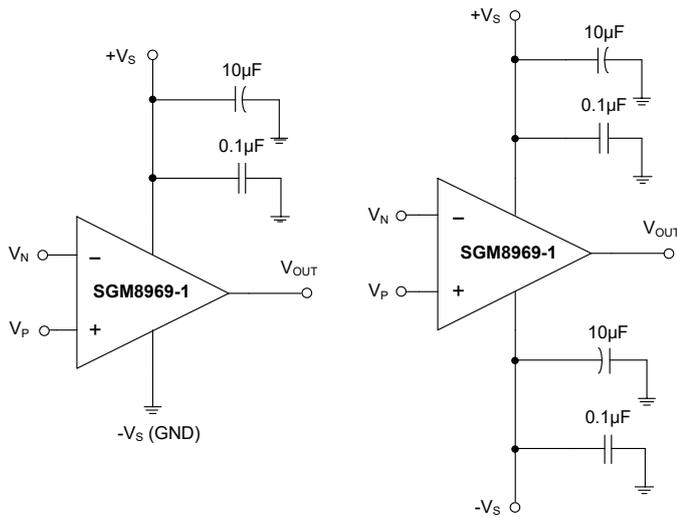
**Grounding**

Separate grounding for analog and digital portions of circuitry is one of the simplest and most effective methods of noise suppression. One or more layers on multilayer PCBs are usually devoted to ground planes.

A ground plane helps distribute heat and reduces EMI noise pickup. Make sure to physically separate digital and analog grounds, paying attention to the flow of the ground current.

**Input-to-Output Coupling**

To minimize capacitive coupling, run the input traces as far away from the supply or output traces as possible. If these traces cannot be kept separate, crossing the sensitive trace perpendicular is much better as opposed to in parallel with the noisy trace. This helps reduce unwanted positive feedback.



**Figure 4. Amplifier with Bypass Capacitors**

## TYPICAL APPLICATION CIRCUITS

### Difference Amplifier

The circuit shown in Figure 5 performs the difference function. If the resistor ratios are equal ( $R_4/R_3 = R_2/R_1$ ), then  $V_{OUT} = (V_P - V_N) \times R_2/R_1 + V_{REF}$ .

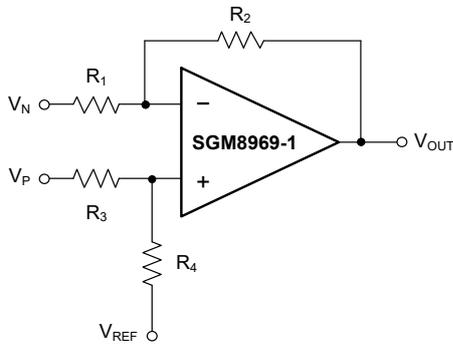


Figure 5. Difference Amplifier

### High Input Impedance Difference Amplifier

The circuit in Figure 6 performs the same function as that in Figure 5 but with a high input impedance.

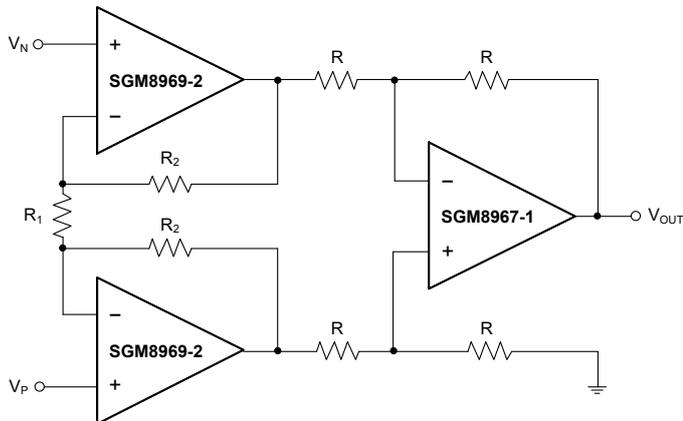


Figure 6. High Input Impedance Difference Amplifier

### Active Low-Pass Filter

The low-pass filter shown in Figure 7 has a DC gain of  $(-R_2/R_1)$  and the  $-3\text{dB}$  corner frequency is  $1/2\pi R_2 C$ . Make sure the filter bandwidth is within the bandwidth of the amplifier. Feedback resistors with large values can couple with parasitic capacitance and cause undesired effects such as ringing or oscillation in high-speed amplifiers. Keep resistor values as low as possible and consistent with output loading consideration.

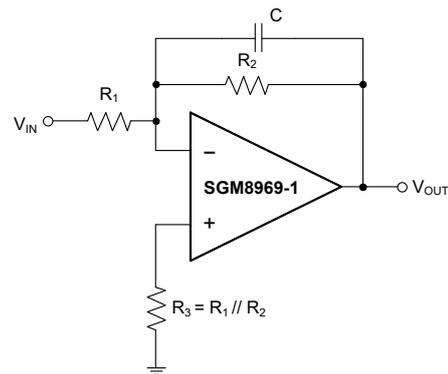


Figure 7. Active Low-Pass Filter

## REVISION HISTORY

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

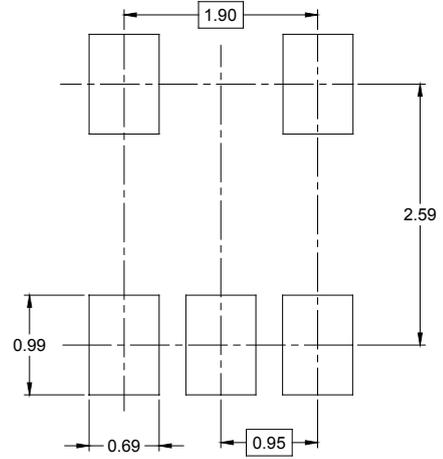
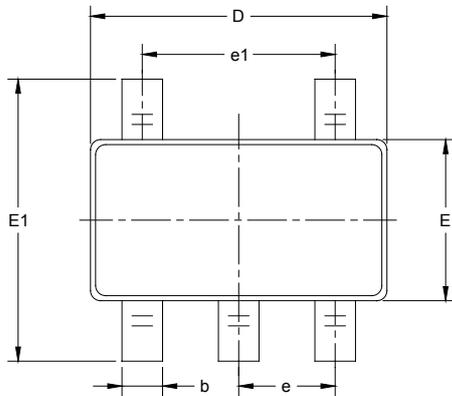
Changes from Original (DECEMBER 2019) to REV.A

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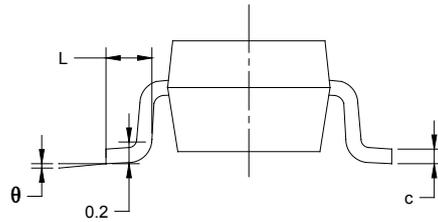
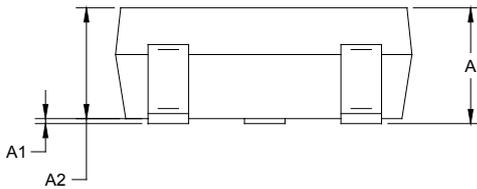
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PACKAGE OUTLINE DIMENSIONS

SOT-23-5



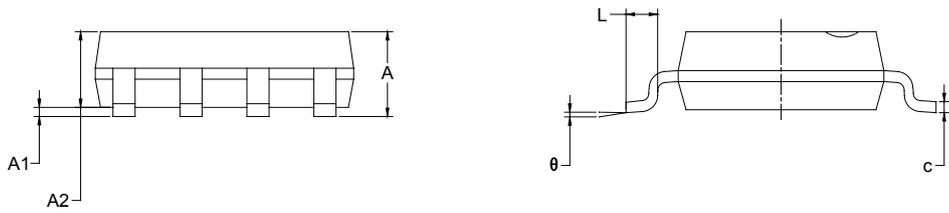
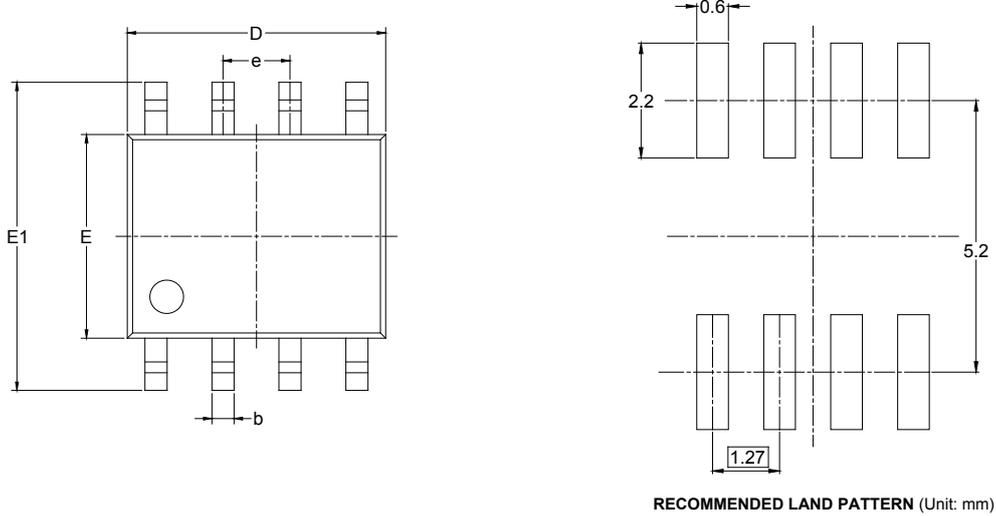
RECOMMENDED LAND PATTERN (Unit: mm)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950 BSC		0.037 BSC	
e1	1.900 BSC		0.075 BSC	
L	0.300	0.600	0.012	0.024
$\theta$	0°	8°	0°	8°

PACKAGE OUTLINE DIMENSIONS

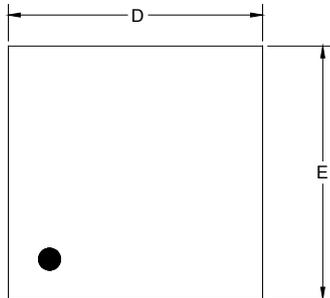
SOIC-8



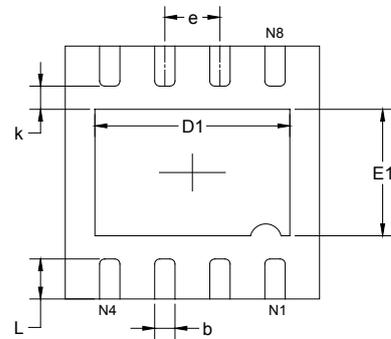
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.27 BSC		0.050 BSC	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

PACKAGE OUTLINE DIMENSIONS

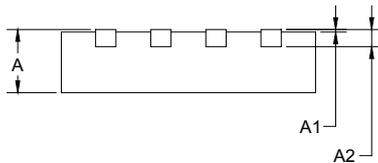
TDFN-3x3-8L



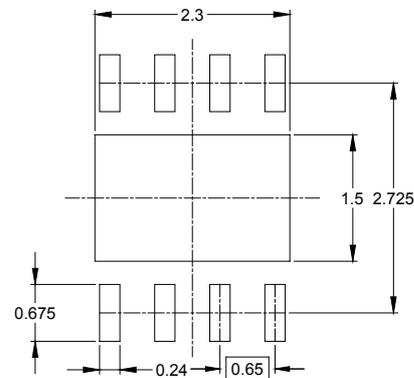
TOP VIEW



BOTTOM VIEW



SIDE VIEW

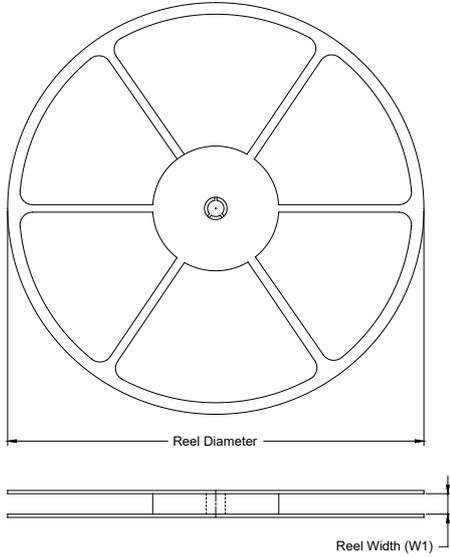


RECOMMENDED LAND PATTERN (Unit: mm)

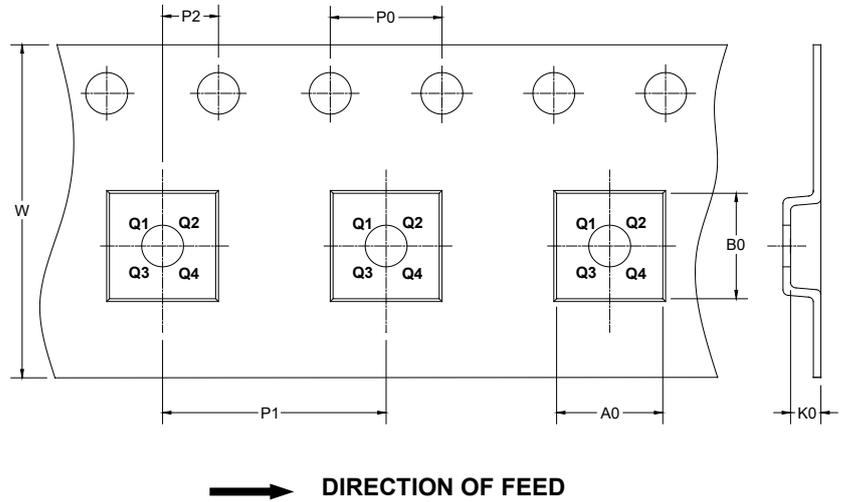
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A2	0.203 REF		0.008 REF	
D	2.900	3.100	0.114	0.122
D1	2.200	2.400	0.087	0.094
E	2.900	3.100	0.114	0.122
E1	1.400	1.600	0.055	0.063
k	0.200 MIN		0.008 MIN	
b	0.180	0.300	0.007	0.012
e	0.650 TYP		0.026 TYP	
L	0.375	0.575	0.015	0.023

## TAPE AND REEL INFORMATION

### REEL DIMENSIONS



### TAPE DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

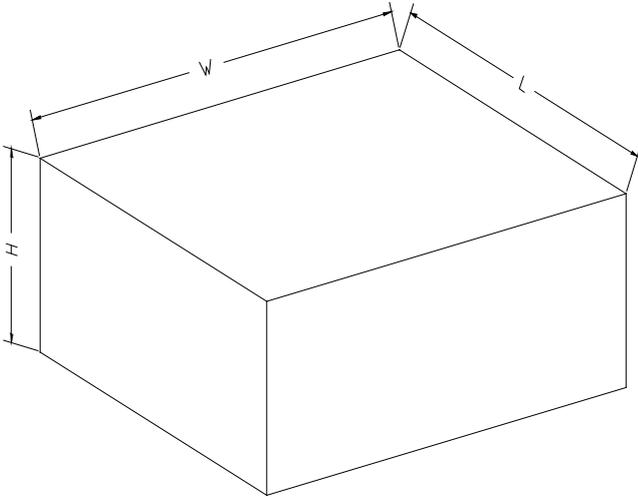
### KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOT-23-5	7"	9.5	3.20	3.20	1.40	4.0	4.0	2.0	8.0	Q3
SOIC-8	13"	12.4	6.40	5.40	2.10	4.0	8.0	2.0	12.0	Q1
TDFN-3×3-8L	13"	12.4	3.35	3.35	1.13	4.0	8.0	2.0	12.0	Q1

DD0001

# PACKAGE INFORMATION

## CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

## KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7" (Option)	368	227	224	8
7"	442	410	224	18
13"	386	280	370	5

DD0002