

SGM4995 1.3W Mono Fully Differential Audio Power Amplifier

GENERAL DESCRIPTION

The SGM4995 is a fully differential audio power amplifier that is designed for portable communication device applications and demanding applications in mobile phones. It is capable of delivering 1.3W of continuous average power into an 8Ω load with typically 1% distortion (THD+N) from a 5V battery voltage. It operates from 2.5V to 5.5V power supply.

The SGM4995 features a low power consumption shutdown mode. To facilitate this, shutdown may be enabled by logic low. Additionally, the SGM4995 features an internal thermal shutdown protection mechanism.

The SGM4995 contains advanced pop/click circuitry, with a minimal amount of external components. All these features make SGM4995 ideal for wireless handsets and other low voltage applications where minimal power consumption is a primary requirement.

The SGM4995 is available in Green TDFN-2×2-8L package. It operates over an ambient temperature range of -40 $^{\circ}$ C to +85 $^{\circ}$ C.

FEATURES

- Fully Differential Amplifier
- Excellent PSRR: Direct Connection to Battery
- 1.3W into 8Ω Load from 5V Supply at THD+N = 1% (TYP)
- 2.5V to 5.5V Operation
- Low Shutdown Current
- Improved Pop/Click Circuitry
- Support Single-Ended or Differential Input
- Thermal Overload Protection Circuitry
- No Output Coupling Capacitors, Bootstrap Capacitors Required
- External Gain Configuration Capability
- -40°C to +85°C Operating Temperature Range
- Available in Green TDFN-2×2-8L Package

APPLICATIONS

Portable Systems Wireless Handsets Mobile Phone Handheld Computers PDAs GPS



1.3W Mono Fully Differential Audio Power Amplifier

PACKAGE/ORDERING INFORMATION

MODEL	ORDER NUMBER	PACKAGE DESCRIPTION	PACKAGE OPTION	MARKING INFORMATION	
SGM4995	SGM4995YDE8G/TR	TDFN-2×2-8L	Tape and Reel, 3000	4995	

ABSOLUTE MAXIMUM RATINGS

Supply Voltage	6V
Input Voltage	0.3V to (V _{CC}) + 0.3V
Storage Temperature Range	65°C to +150°C
Junction Temperature	150°C
Operating Temperature Range	-40°C to +85°C
Lead Temperature Range (Soldering 10)sec)
	260°C
ESD Susceptibility	
HBM	2000V
MM	400V

CAUTION

This integrated circuit can be damaged by ESD if you don't pay attention to ESD protection. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

SGMICRO reserves the right to make any change in circuit design, specification or other related things if necessary without notice at any time. Please contact SGMICRO sales office to get the latest datasheet.

PIN CONFIGURATION (TOP VIEW)



NOTE:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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ELECTRICAL CHARACTERISTICS

(The following AC specifications apply for $8\Omega \log A_V = 1V/V$, $T_A = +25^{\circ}C$, unless otherwise specified.)

PARAMETER	PARAMETER SYMBOL CONDITIONS		NDITIONS	MIN	TYP	MAX	UNITS	
Supply Voltage	V _{cc}	V _{cc}		2.5		5.5	V	
Shutdown Current	I _{SD}	V _{IN} = 0V, V _{SHDN} = GN	ND		0.02	2	μA	
		$V_{IN} = 0V, V_{SHDN} = V_{O}$	_{cc} = 5.0V	-10	2.5	10	mV	
Output Offset Voltage	Vos	$V_{IN} = 0V, V_{SHDN} = V_{O}$	_{cc} = 3.3V	-10	2.0	10		
		$V_{IN} = 0V, V_{SHDN} = V_{O}$	_{cc} = 2.6V		2.0			
		V _{IN} = 0V, I _O = 0A,	V _{CC} = 5.0V, No Load		4.72	8		
			V_{CC} = 5.0V, 8 Ω Load		4.75	8.2		
Quieseent Dewer Supply Current			V _{CC} = 3.3V, No Load		3.70	6		
Quiescent Power Supply Current	lα	$V_{SHDN} = V_{CC}$	V _{CC} = 3.3V, 8Ω Load		3.72		mA	
			V _{CC} = 2.6V, No Load		2.90		-	
			V_{CC} = 2.6V, 8 Ω Load		3.00			
Shutdown Voltage Input High	V _{SDIH}			1.2			v	
Shutdown Voltage Input Low	V _{SDIL}					0.4	v	
			V _{CC} = 5.0V		1.30		- W	
		f = 1kHz,	V _{CC} = 3.6V		0.65			
	Po	THD+N = 1%	V _{CC} = 3.0V		0.47			
			V _{CC} = 2.6V		0.34			
Output Power (8Ω)		f = 1kHz, THD+N = 10%	V _{CC} = 5.0V		1.60			
			V _{CC} = 3.6V		0.84			
			V _{CC} = 3.0V		0.58			
			V _{CC} = 2.6V		0.42			
Total Harmonic Distortion + Noise	THD+N	P _o = 0.6Wrms, f = 1kHz, V _{CC} = 5.0V			0.042		%	
	ion Ratio ^{(1) (2)} PSRR		V _{CC} = 5.0V		-62			
		6 04711	V _{CC} = 3.6V		-60			
		f = 217Hz	V _{CC} = 3.0V		-58			
$\mathbf{D}_{\mathbf{r}}$ and $\mathbf{D}_{\mathbf{r}}$ is a fixed $\mathbf{D}_{\mathbf{r}}$ (1)(2)			V _{CC} = 2.6V		-57			
Power Supply Rejection Ratio (1) (2)		f = 1kHz	V _{CC} = 5.0V		-73			
			V _{CC} = 3.6V		-71			
			V _{CC} = 3.0V		-70			
			V _{CC} = 2.6V		-63			
Common Mode Rejection Ratio ⁽²⁾	CMRR	f = 217Hz, V _{CM} = 2	f = 217Hz, V _{CM} = 200mV _{P-P} , V _{CC} = 5.0V		-74		dB	
			V _{CC} = 5.0V		50			
Wales Ha Time	_	С _в = 1µF	V _{CC} = 3.6V		42			
Wake-Up Time	T _{WU}		V _{CC} = 3.0V		37		— ms	
			V _{CC} = 2.6V		32			

NOTES:

1. 10Ω terminated input.

2. PSRR and CMRR are affected by the matching between gain-setting resistor ratios.



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TYPICAL PERFORMANCE CHARACTERISTICS

At T_A = +25°C, A_V = 1, f = 1kHz, C_B = 1µF, unless otherwise noted.



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At T_A = +25°C, A_V = 1, f = 1kHz, C_B = 1 μ F, unless otherwise noted.





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APPLICATION CIRCUITS



Figure 1. Typical Differential Input Application Schematic





1. A 10k Ω resistor must be serially connected to $\overline{_{SHDN}}$ pin.



NOTE:

APPLICATION NOTES

PCB Design Recommendations (Thermal Design Considerations)

Thermal Land

The TDFN-2×2-8L thermal land is a metal (normally copper) region centrally located under the package and on top of the PCB. It has a rectangular or square shape and should match the dimensions of the exposed pad on the bottom of the package (1:1 ratio).

For certain high power applications, the PCB land may be modified to a "dog bone" shape that enhances thermal performance. The packages used with the "dog bone" lands will be a dual inline configuration (see Figure 3).



Figure 3. Dog Bone

Thermal Vias

Thermal vias are necessary. They conduct heat from the exposed pad of the package to the ground plane. The number of vias is application specific and is dependent upon electrical requirements and power dissipation.

The via diameter should be 0.2mm to 0.33mm with 1oz. copper via barrel plating. It is important to plug the via to avoid any solder wicking inside the via during the soldering process. The thermal vias can be tented with solder mask on the top surface of the PCB. The solder mask diameter should be at least 75microns (or 3mils) larger than the via diameter. The solder mask thickness should be the same across the entire PCB.

A package thermal performance may be improved by increasing the number of vias.



PACKAGE OUTLINE DIMENSIONS

TDFN-2×2-8L



TOP VIEW









RECOMMENDED LAND PATTERN (Unit: mm)

Symbol		nsions imeters	Dimensions In Inches		
	MIN	MAX	MIN	MAX	
A	0.700	0.800	0.028	0.031	
A1	0.000	0.050	0.000	0.002	
A2	0.203 REF 0.008 REF			REF	
D	1.900	2.100	0.075	0.083	
D1	1.100	1.300	0.043	0.051	
E	1.900	2.100	0.075	0.083	
E1	0.500	0.700	0.020	0.028	
k	0.200 MIN		0.008 MIN		
b	0.180	0.300	0.007	0.012	
е	0.500 TYP		0.020 TYP		
L	0.250	0.450 0.010 0		0.018	



TAPE AND REEL INFORMATION

REEL DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
TDFN-2×2-8L	7″	9.5	2.30	2.30	1.10	4.00	4.00	2.00	8.00	Q1



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CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7" (Option)	368	227	224	8
7" 442		410	224	18

