



SGM3005C

Ultra Low On-Resistance, Low Voltage, Dual, SPDT Analog Switch

GENERAL DESCRIPTION

The SGM3005C is a dual, low on-resistance, low voltage, bidirectional, single-pole/double-throw (SPDT) CMOS analog switch designed to operate from a single 1.8V to 5.5V power supply. Targeted applications include battery-powered equipment that benefit from low R_{ON} (0.6Ω) and fast switching speeds ($t_{ON} = 50\text{ns}$, $t_{OFF} = 15\text{ns}$).

The on-resistance profile is very flat over the full analog signal range. This ensures excellent linearity and low distortion when switching audio signals.

The SGM3005C is a committed dual single-pole/double-throw (SPDT) that consists of two normally open (NO) and two normally close (NC) switches. This configuration can be used as a dual 2-to-1 multiplexer.

The SGM3005C is available in Green MSOP-10 package.

FEATURES

- **Low Voltage Operation: 1.8V to 5.5V**
- **Low On-Resistance: 0.6Ω (TYP)**
- **Low On-Resistance Flatness**
- **-3dB Bandwidth: 15MHz**
- **Fast Switching Times**
 - $t_{ON} \leq 50\text{ns}$
 - $t_{OFF} \leq 15\text{ns}$
- **Rail-to-Rail Operation**
- **Typical Power Consumption (< 0.01μW)**
- **TTL/CMOS Compatible**
- **Microsize Package**

APPLICATIONS

Battery-Powered, Handheld, and Portable Equipment
Cellular/Mobile Phones
Laptops, Notebooks, Palmtops
Communication Systems
Sample-and-Hold Circuits
Audio Signal Routing
Audio and Video Switching
Portable Test and Measurement
Medical Equipment

PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
SGM3005C	MSOP-10	-40°C to +125°C	SGM3005CXMS10G/TR	SGM3005 XMS XXXXX	Tape and Reel, 4000

MARKING INFORMATION

NOTE: XXXXX = Date Code and Vendor Code.

XXXXX

Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

V ₊ to GND	-0.3V to 6V
Analog, Digital voltage range ⁽¹⁾	-0.3V to (V ₊) + 0.3V
Continuous Current NO, NC, or COM	±300mA
Peak Current NO, NC, or COM	±500mA
Package Thermal Resistance @ T _A = +25°C	
MSOP-10, θ _{JA}	150°C/W
Junction Temperature	+150°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (Soldering, 10s)	+260°C
ESD Susceptibility	
HBM	1500V
MM	200V

NOTE:

1. Signals on NC, NO, or COM or IN exceeding V₊ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.

RECOMMENDED OPERATING CONDITIONS

Operating Temperature Range

-40°C to +125°C

OVERSTRESS CAUTION

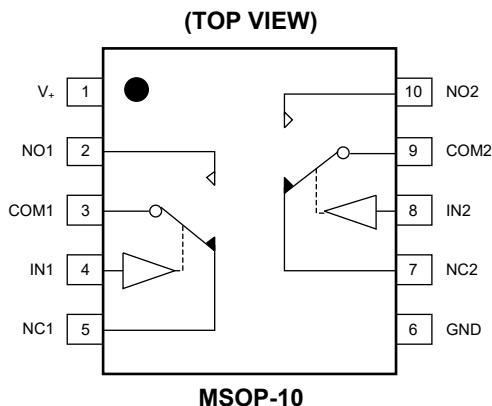
Stresses beyond those listed may cause permanent damage to the device. Functional operation of the device at these or any other conditions beyond those indicated in the operational section of the specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

ESD SENSITIVITY CAUTION

This integrated circuit can be damaged by ESD if you don't pay attention to ESD protection. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, specification or other related things if necessary without notice at any time.

PIN CONFIGURATION**PIN DESCRIPTION**

PIN	NAME	FUNCTION
1	V ₊	Power Supply.
2, 10	NO1, NO2	Normally-Open Terminal.
3, 9	COM1, COM2	Common Terminal.
4, 8	IN1, IN2	Digital Control Pin to Connect the COM Terminal to the NO or NC Terminals.
5, 7	NC1, NC2	Normally-Closed Terminal.
6	GND	Ground.

NOTE: NO, NC and COM terminals may be an input or output.

FUNCTION TABLE

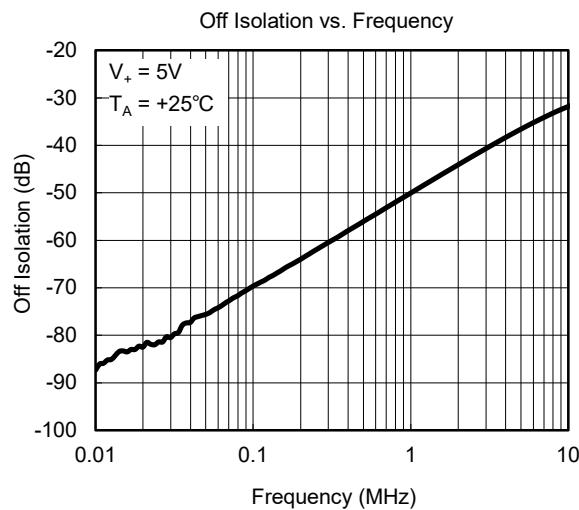
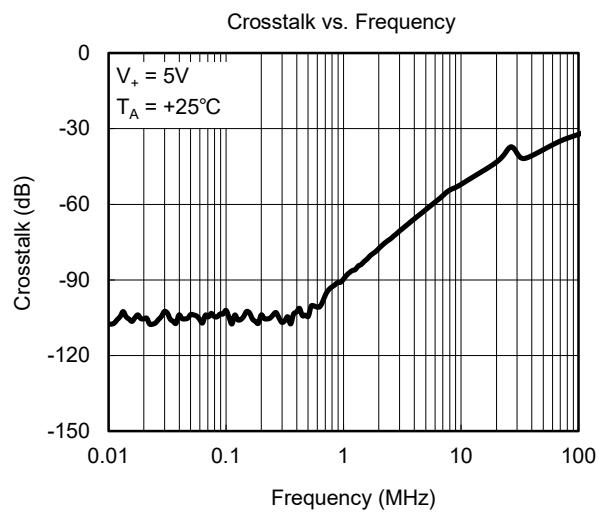
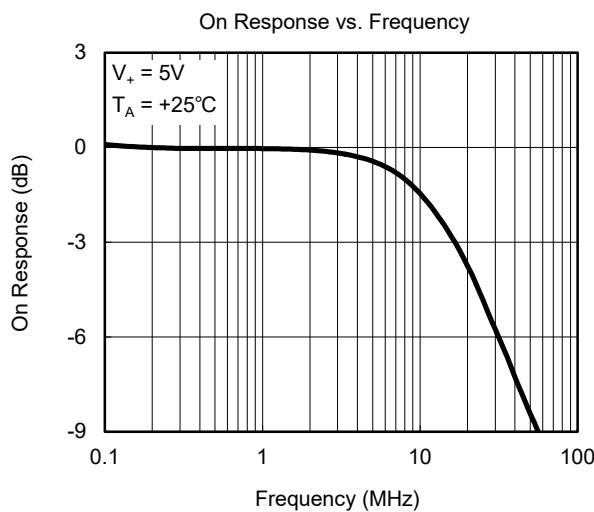
LOGIC	NC1, NC2	NO1, NO2
0	ON	OFF
1	OFF	ON

SGM3005C**Ultra Low On-Resistance,
Low Voltage, Dual, SPDT Analog Switch****ELECTRICAL CHARACTERISTICS**(At $T_A = +25^\circ\text{C}$, $V_+ = 5\text{V} \pm 10\%$, Full = -40°C to $+125^\circ\text{C}$, unless otherwise noted.)

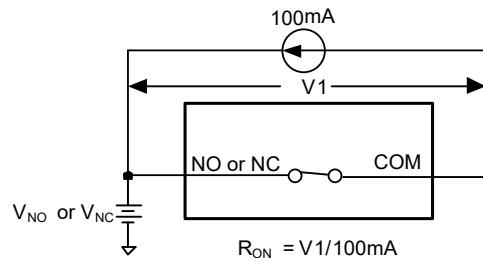
PARAMETER	SYMBOL	CONDITIONS	SGM3005C			
			+25°C	Full	UNITS	MIN/MAX
ANALOG SWITCH						
Analog Signal Range	V_{NO}, V_{NC}, V_{COM}			0	V	MIN
				V_+	V	MAX
On-Resistance	R_{ON}	$0 \leq V_{NO} \text{ or } V_{NC} \leq V_+, I_{COM} = -100\text{mA}$, Test Circuit 1	0.6		Ω	TYP
			0.95	1.25	Ω	MAX
On-Resistance Match between Channels	ΔR_{ON}	$0 \leq V_{NO} \text{ or } V_{NC} \leq V_+, I_{COM} = -100\text{mA}$, Test Circuit 1	0.05		Ω	TYP
			0.1	0.2	Ω	MAX
On-Resistance Flatness	$R_{FLAT(ON)}$	$0 \leq V_{NO} \text{ or } V_{NC} \leq V_+, I_{COM} = -100\text{mA}$, Test Circuit 1	0.2		Ω	TYP
			0.4	0.45	Ω	MAX
LEAKAGE CURRENTS						
Source Off Leakage Current	$I_{NC(OFF)}, I_{NO(OFF)}$	$V_{NO} \text{ or } V_{NC} = 4.5\text{V}/1\text{V}, V_{COM} = 1\text{V}/4.5\text{V}$, $V_+ = 5.5\text{V}$, Test Circuit 2	± 0.01		μA	TYP
				± 1	μA	MAX
Channel On Leakage Current	$I_{NC(ON)}, I_{NO(ON)},$ $I_{COM(ON)}$	$V_{NO} \text{ or } V_{NC} = V_{COM} = 1\text{V} \text{ or } 4.5\text{V}$, $V_+ = 5.5\text{V}$, Test Circuit 3	± 0.01		μA	TYP
				± 1	μA	MAX
DIGITAL INPUTS						
Input High Voltage	V_{INH}			2.4	V	MIN
Input Low Voltage	V_{INL}			0.6	V	MAX
Input Current	$I_{INL} \text{ or } I_{INH}$	$V_{IN} = V_{INH} \text{ or } V_{INL}$	± 0.01		μA	TYP
				± 1	μA	MAX
DYNAMIC CHARACTERISTICS						
Turn-On Time	t_{ON}	$V_{NO} \text{ or } V_{NC} = 3\text{V}$, $R_L = 300\Omega$, $C_L = 35\text{pF}$, Test Circuit 4	50		ns	TYP
Turn-Off Time	t_{OFF}	$V_{NO} \text{ or } V_{NC} = 3\text{V}$, $R_L = 300\Omega$, $C_L = 35\text{pF}$, Test Circuit 4	15		ns	TYP
Charge Injection	Q	$C_L = 1.0\text{nF}$, $V_G = 0\text{V}$, $R_G = 0\Omega$, Test Circuit 5	20		pC	TYP
Break-Before-Make Time Delay	t_D	$V_{NO1} \text{ or } V_{NC1} = V_{NO2} \text{ or } V_{NC2} = 3\text{V}$, $R_L = 300\Omega$, $C_L = 35\text{pF}$, Test Circuit 6	10		ns	TYP
Off Isolation	O_{ISO}	$R_L = 50\Omega$, $C_L = 5\text{pF}$, Test Circuit 7	$f = 100\text{kHz}$	-69		TYP
			$f = 10\text{kHz}$	-85		TYP
Channel-to-Channel Crosstalk	X_{TALK}	$R_L = 50\Omega$, $C_L = 5\text{pF}$, Test Circuit 8	$f = 100\text{kHz}$	-90		TYP
			$f = 10\text{kHz}$	-105		TYP
Total Harmonic Distortion	THD	$f = 20\text{Hz} \text{ to } 20\text{kHz}$, $V_{COM} = 3.5\text{V}_{\text{P-P}}$, $R_L = 600\Omega$, $C_L = 50\text{pF}$	0.065		%	TYP
-3dB Bandwidth	BW	$R_L = 50\Omega$, $C_L = 5\text{pF}$, Test Circuit 9	15		MHz	TYP
Source Off Capacitance	$C_{NC(OFF)}, C_{NO(OFF)}$		82		pF	TYP
Channel On Capacitance	$C_{NC(ON)}, C_{NO(ON)},$ $C_{COM(ON)}$		380		pF	TYP
POWER REQUIREMENTS						
Power Supply Current	I_+	$V_+ = 5.5\text{V}$, $V_{IN} = 0\text{V}$ or 5.5V	0.001		μA	TYP
				1	μA	MAX

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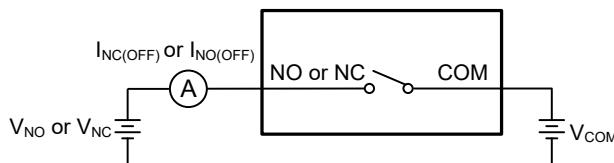
PARAMETER	SYMBOL	CONDITIONS	SGM3005C			
			+25°C	Full	UNITS	MIN/MAX
ANALOG SWITCH						
Analog Signal Range	V_{NO}, V_{NC}, V_{COM}			0	V	MIN
				V_+	V	MAX
On-Resistance	R_{ON}	$0 \leq V_{NO} \text{ or } V_{NC} \leq V_+, I_{COM} = -100\text{mA}, V_+ = 2.7\text{V}, \text{Test Circuit 1}$	0.7		Ω	TYP
			1.05	1.3	Ω	MAX
On-Resistance Match between Channels	ΔR_{ON}	$0 \leq V_{NO} \text{ or } V_{NC} \leq V_+, I_{COM} = -100\text{mA}, V_+ = 2.7\text{V}, \text{Test Circuit 1}$	0.05		Ω	TYP
			0.1	0.2	Ω	MAX
On-Resistance Flatness	$R_{FLAT(ON)}$	$0 \leq V_{NO} \text{ or } V_{NC} \leq V_+, I_{COM} = -100\text{mA}, V_+ = 2.7\text{V}, \text{Test Circuit 1}$	0.25		Ω	TYP
			0.45	0.5	Ω	MAX
LEAKAGE CURRENTS						
Source Off Leakage Current	$I_{NC(OFF)}, I_{NO(OFF)}$	$V_{NO} \text{ or } V_{NC} = 3\text{V}/1\text{V}, V_{COM} = 1\text{V}/3\text{V}, V_+ = 3.3\text{V}, \text{Test Circuit 2}$	± 0.01		μA	TYP
				± 1	μA	MAX
Channel On Leakage Current	$I_{NC(ON)}, I_{NO(ON)}, I_{COM(ON)}$	$V_{NO} \text{ or } V_{NC} = V_{COM} = 1\text{V} \text{ or } 3\text{V}, V_+ = 3.3\text{V}, \text{Test Circuit 3}$	± 0.01		μA	TYP
				± 1	μA	MAX
DIGITAL INPUTS						
Input High Voltage	V_{INH}			2	V	MIN
Input Low Voltage	V_{INL}			0.4	V	MAX
Input Current	$I_{INL} \text{ or } I_{INH}$	$V_{IN} = V_{INH} \text{ or } V_{INL}$	± 0.01		μA	TYP
				± 1	μA	MAX
DYNAMIC CHARACTERISTICS						
Turn-On Time	t_{ON}	$V_{NO} \text{ or } V_{NC} = 2\text{V}, R_L = 300\Omega, C_L = 35\text{pF}, \text{Test Circuit 4}$	50		ns	TYP
Turn-Off Time	t_{OFF}	$V_{NO} \text{ or } V_{NC} = 2\text{V}, R_L = 300\Omega, C_L = 35\text{pF}, \text{Test Circuit 4}$	17		ns	TYP
Charge Injection	Q	$C_L = 1.0\text{nF}, V_G = 0\text{V}, R_G = 0\Omega, \text{Test Circuit 5}$	25		pC	TYP
Break-Before-Make Time Delay	t_D	$V_{NO1} \text{ or } V_{NC1} = V_{NO2} \text{ or } V_{NC2} = 2\text{V}, R_L = 300\Omega, C_L = 35\text{pF}, \text{Test Circuit 6}$	11		ns	TYP
Off Isolation	O_{ISO}	$R_L = 50\Omega, C_L = 5\text{pF}, \text{Test Circuit 7}$	$f = 100\text{kHz}$	-69		TYP
			$f = 10\text{kHz}$	-85		TYP
Channel-to-Channel Crosstalk	X_{TALK}	$R_L = 50\Omega, C_L = 5\text{pF}, \text{Test Circuit 8}$	$f = 100\text{kHz}$	-90		TYP
			$f = 10\text{kHz}$	-105		TYP
Total Harmonic Distortion	THD	$f = 20\text{Hz} \text{ to } 20\text{kHz}, V_{COM} = 2V_{P-P}, R_L = 600\Omega, C_L = 50\text{pF}$	0.06		%	TYP
-3dB Bandwidth	BW	$R_L = 50\Omega, C_L = 5\text{pF}, \text{Test Circuit 9}$	15		MHz	TYP
Source Off Capacitance	$C_{NC(OFF)}, C_{NO(OFF)}$			82		pF
Channel On Capacitance	$C_{NC(ON)}, C_{NO(ON)}, C_{COM(ON)}$			380		pF
POWER REQUIREMENTS						
Power Supply Current	I_+	$V_+ = 3.3\text{V}, V_{IN} = 0\text{V} \text{ or } 3.3\text{V}$	0.001		μA	TYP
				1	μA	MAX

TYPICAL PERFORMANCE CHARACTERISTICS

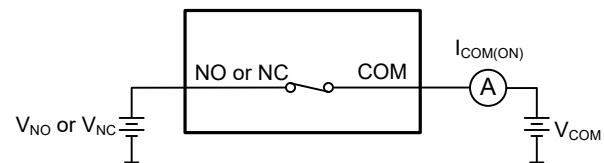
TEST CIRCUITS



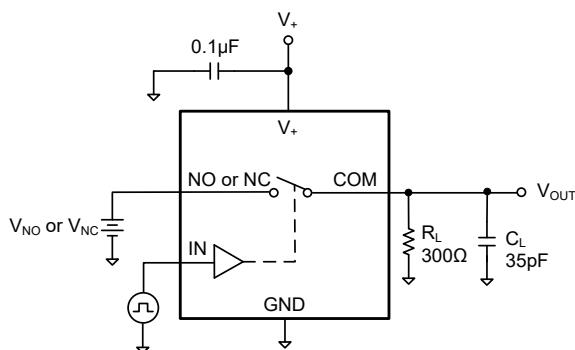
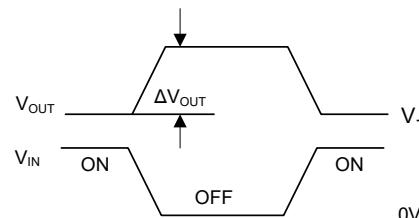
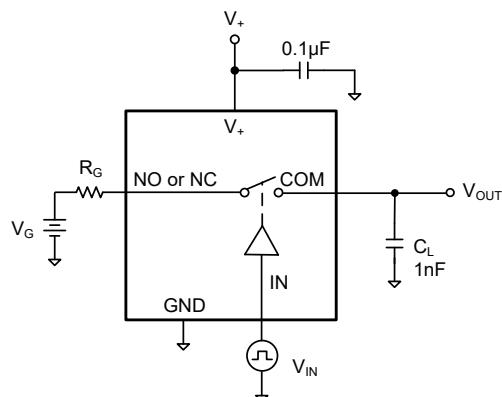
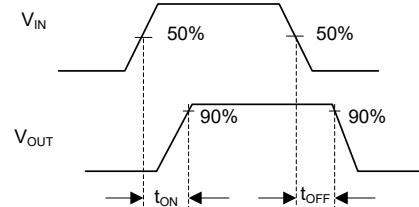
Test Circuit 1. On-Resistance



Test Circuit 2. Off Leakage

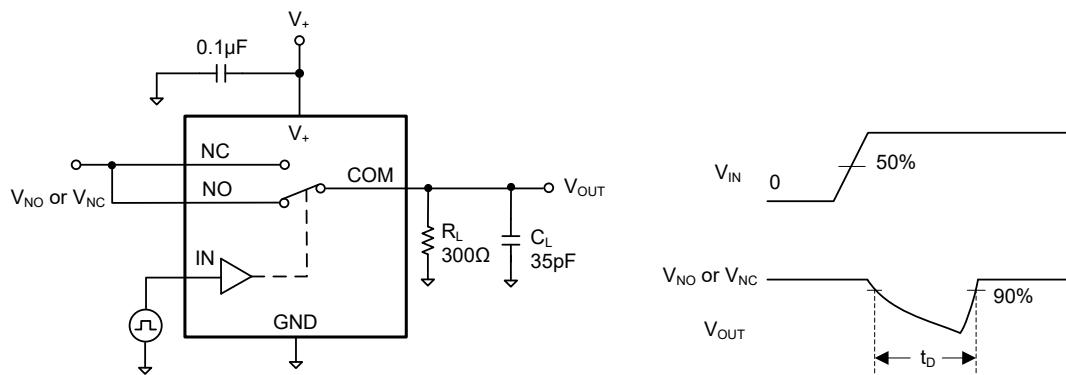
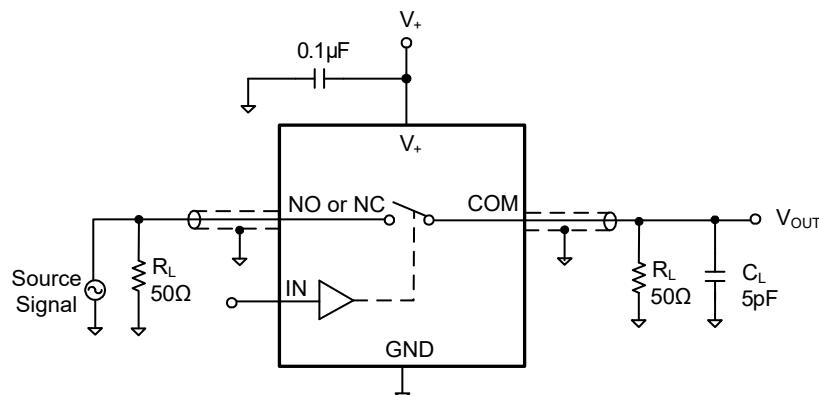


Test Circuit 3. On Leakage

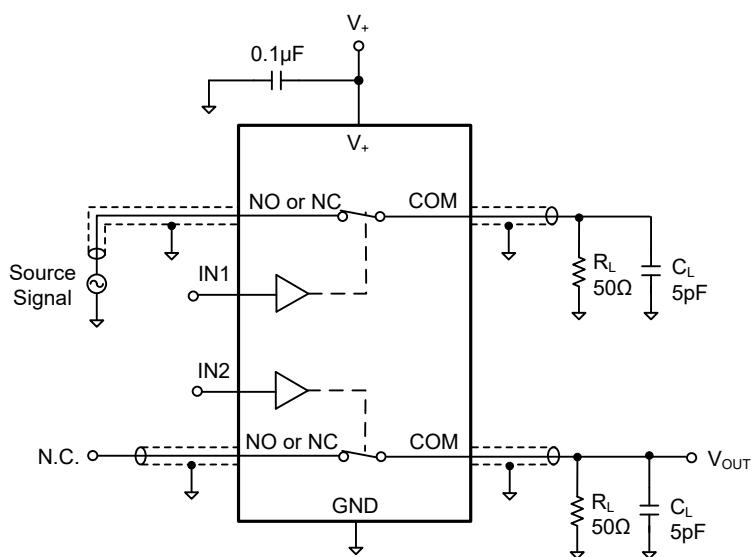
Test Circuit 4. Switching Times (t_{ON} , t_{OFF})

Test Circuit 5. Charge Injection

TEST CIRCUITS (continued)

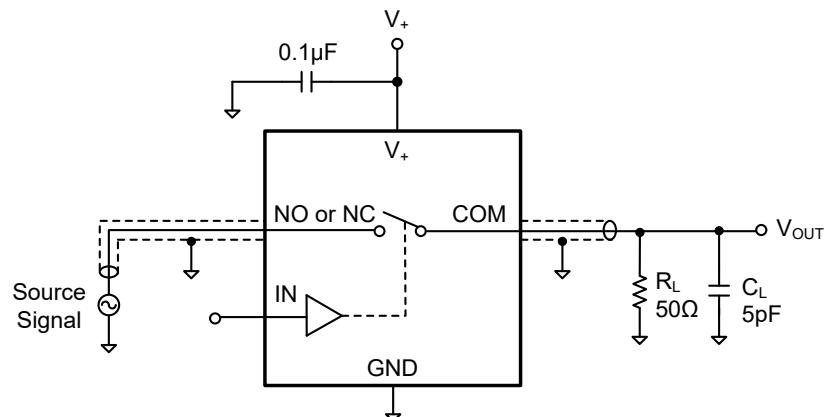
Test Circuit 6. Break-Before-Make Time Delay (t_D)

Test Circuit 7. Off Isolation



$$\text{Channel To Channel Crosstalk} = -20 \times \log \frac{V_{NO} \text{ or } V_{NC}}{V_{OUT}}$$

Test Circuit 8. Channel-to-Channel Crosstalk

TEST CIRCUITS (continued)

Test Circuit 9. -3dB Bandwidth

REVISION HISTORY

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

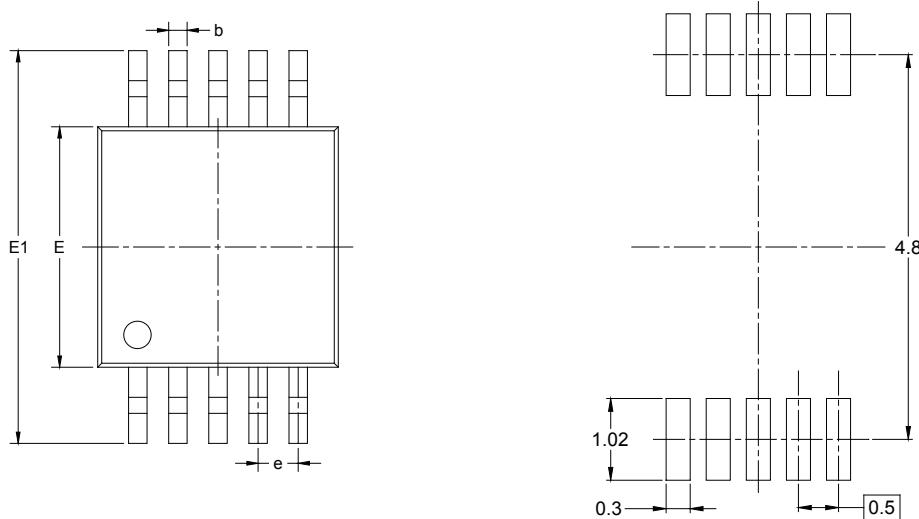
Changes from Original (JUNE 2018) to REV.A

Changed from product preview to production data.....All

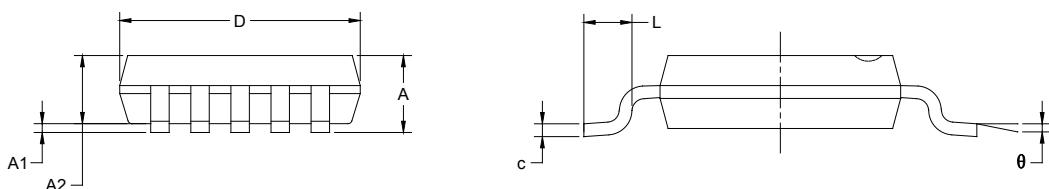
PACKAGE INFORMATION

PACKAGE OUTLINE DIMENSIONS

MSOP-10



RECOMMENDED LAND PATTERN (Unit: mm)

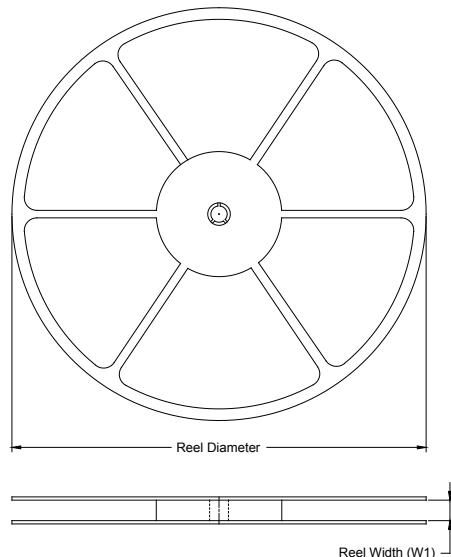


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.820	1.100	0.032	0.043
A1	0.020	0.150	0.001	0.006
A2	0.750	0.950	0.030	0.037
b	0.180	0.280	0.007	0.011
c	0.090	0.230	0.004	0.009
D	2.900	3.100	0.114	0.122
E	2.900	3.100	0.114	0.122
E1	4.750	5.050	0.187	0.199
e	0.500 BSC		0.020 BSC	
L	0.400	0.800	0.016	0.031
θ	0°	6°	0°	6°

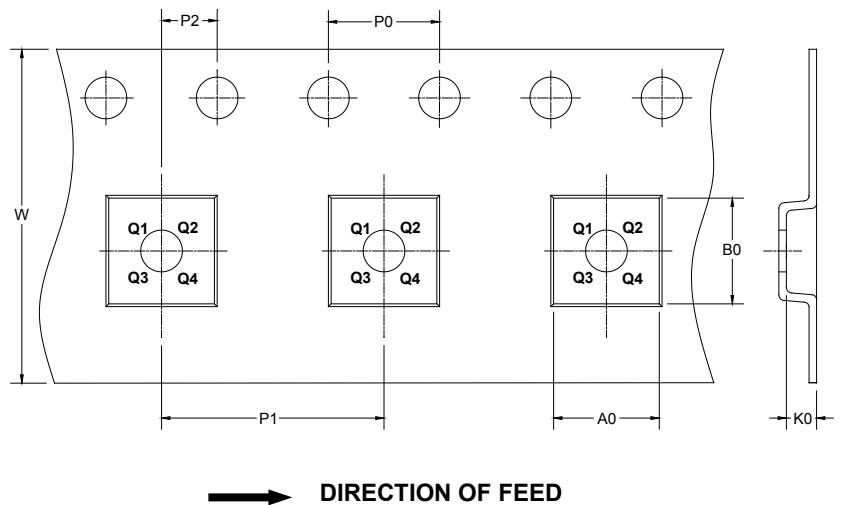
PACKAGE INFORMATION

TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant	DD0001
MSOP-10	13"	12.4	5.20	3.30	1.20	4.0	8.0	2.0	12.0	Q1	

PACKAGE INFORMATION

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
13"	386	280	370	5

00002