



SGM3719 **4Ω, 400MHz Bandwidth, SPDT** **Negative Signal Handling Analog Switch**

GENERAL DESCRIPTION

The SGM3719 is a bidirectional, single-pole/double-throw (SPDT) CMOS analog switch designed to operate from a single +2.5V to +5.0V supply. It features high bandwidth (400MHz) and low on-resistance (4Ω TYP), and the switches can handle negative signal down to -2.0V.

The SGM3719 features a guaranteed on-resistance matching of 0.1Ω (TYP) between switches and a guaranteed on-resistance flatness of 1Ω (TYP) over the signal range. This ensures excellent linearity and low distortion when switching audio signals.

The SGM3719 is a committed single-pole/double-throw (SPDT) switch that consists of one normally open (NO) and one normally closed (NC) switches. This configuration can be used as a 2-to-1 multiplexer.

The SGM3719 is available in Green SOT-23-6 package.

FEATURES

- **Supply Range: +2.5V to +5.0V**
- **Negative Signal Swing Capability: -2.0V to V_+**
- **On-Resistance: 4Ω (TYP) at +5.0V**
- **High Bandwidth: 400MHz**
- **Fast Switching Times**
 - $t_{ON} = 15\text{ns}$ (TYP)
 - $t_{OFF} = 11\text{ns}$ (TYP)
- **High Off-Isolation: -49dB at 10MHz**
- **1.8V Logic Control**
- **Break-Before-Make Switching**
- **Extended Industrial Temperature Range: -40°C to +85°C**
- **Small Package**
 - Available in Green SOT-23-6 Package**

APPLICATIONS

Portable Instrumentation
Battery-Operated Equipment
Computer Peripherals
Cell Phones
PDAs
MP3s



SGM3719

4Ω, 400MHz Bandwidth, SPDT Negative Signal Handling Analog Switch

PACKAGE/ORDERING INFORMATION

MODEL	ORDERING NUMBER	PIN-PACKAGE	SPECIFIED TEMPERATURE RANGE	PACKAGE MARKING	PACKAGE OPTION
SGM3719	SGM3719YN6G/TR	SOT-23-6	-40°C to +85°C	SOEXX	Tape and Reel, 3000

NOTE: XX = Date Code.

MARKING INFORMATION

SOE X X

_____ Date code - Month ("A" = Jan, "B" = Feb. ... "L" = Dec.)

_____ Date code - Year ("A" = 2010, "B" = 2011 ...)

_____ Chip I.D.

For example: SOEDA (2013, January)

ABSOLUTE MAXIMUM RATINGS

V ₊ , IN to GND.....	-0.3V to +6V
Analog, Digital Voltage Range ⁽²⁾ (V ₊) - 6.0V to (V ₊) + 0.3V	
Continuous Current NO, NC, or COM.....	±50mA
Peak Current NO, NC, or COM	±80mA
Operating Temperature Range.....	-40°C to +85°C
Junction Temperature.....	150°C
Storage Temperature Range.....	-65°C to +150°C
Lead Temperature (Soldering, 10s).....	260°C
ESD Susceptibility	
HBM.....	6000V
MM.....	250V

NOTES:

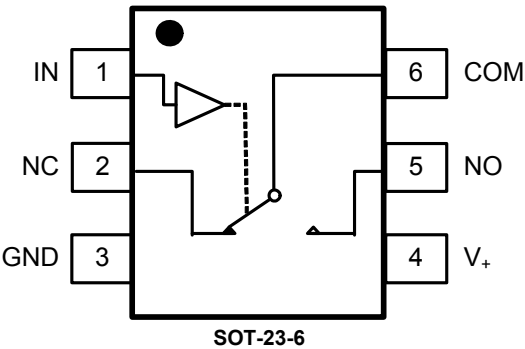
1. Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.
2. Signals on NC, NO, or COM or IN exceeding V₊ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.

CAUTION

This integrated circuit can be damaged by ESD if you don't pay attention to ESD protection. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

SGMICRO reserves the right to make any change in circuit design, specification or other related things if necessary without notice at any time. Please contact SGMICRO sales office to get the latest datasheet.

PIN CONFIGURATION (TOP VIEW)



PIN DESCRIPTION

PIN	NAME	FUNCTION
1	IN	Digital Control Pin to Connect the COM Terminal to the NO or NC Terminals.
2	NC	Normally-Closed Terminal.
3	GND	Ground.
4	V ₊	Power Supply.
5	NO	Normally-Open Terminal.
6	COM	Common Terminal.

NOTE: NO, NC and COM terminals may be an input or output.

FUNCTION TABLE

LOGIC	NO	NC
0	OFF	ON
1	ON	OFF

NOTE: Switches shown for logic “0” input.

SGM3719

4Ω, 400MHz Bandwidth, SPDT Negative Signal Handling Analog Switch

ELECTRICAL CHARACTERISTICS

(V₊ = +4.5V to +5.0V, Full = -40°C to +85°C, typical values are at V₊ = 5.0V, T_A = +25°C, unless otherwise noted.)

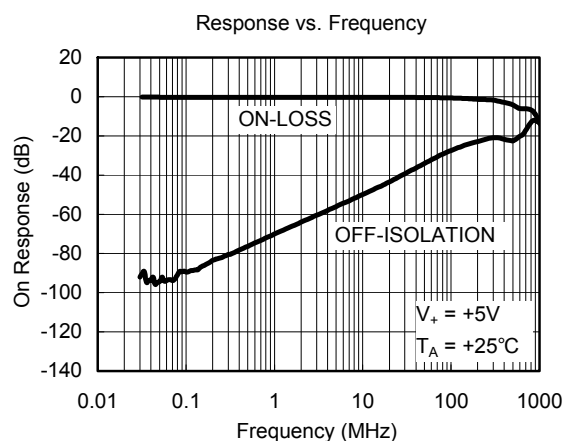
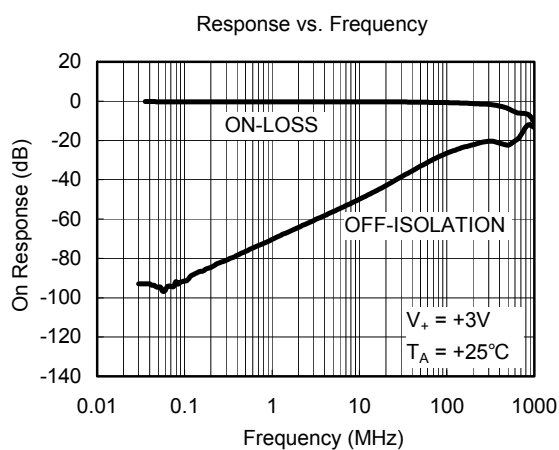
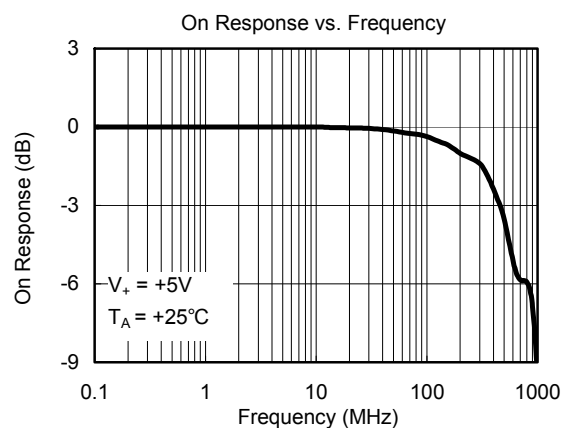
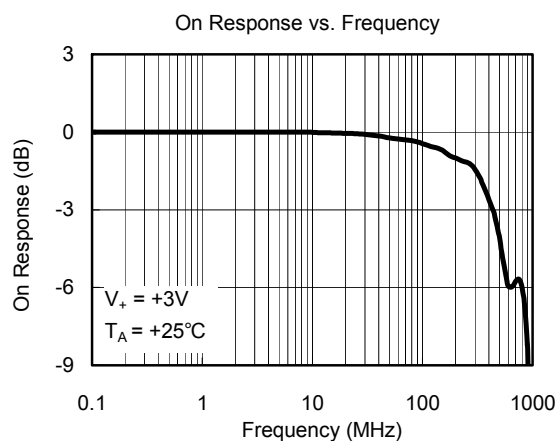
PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V _{NO} , V _{NC} , V _{COM}	2.5V ≤ V ₊ ≤ 3.5V	Full	-2		V ₊	V
		3.5V ≤ V ₊ ≤ 5.0V		(V ₊) - 5.5		V ₊	
On-Resistance	R _{ON}	V ₊ = 4.5V, 0V ≤ V _{NO} or V _{NC} ≤ V ₊ , I _{COM} = -10mA, Test Circuit 1	+25°C		4	6	Ω
			Full			6.5	
On-Resistance Match Between Channels	ΔR _{ON}	V ₊ = 4.5V, 0V ≤ V _{NO} or V _{NC} ≤ V ₊ , I _{COM} = -10mA, Test Circuit 1	+25°C		0.1	0.55	Ω
			Full			0.6	
On-Resistance Flatness	R _{FLAT(ON)}	V ₊ = 4.5V, 0V ≤ V _{NO} or V _{NC} ≤ V ₊ , I _{COM} = -10mA, Test Circuit 1	+25°C		1	2	Ω
			Full			2.5	
Source OFF Leakage Current	I _{NC(OFF)} , I _{NO(OFF)}	V ₊ = 5.0V, V _{NO} or V _{NC} = 1.0V, 4.5V, V _{COM} = 4.5V, 1.0V	Full			1	μA
Channel ON Leakage Current	I _{NC(ON)} , I _{NO(ON)} , I _{COM(ON)}	V ₊ = 5.0V, V _{COM} = 1.0V, 4.5V, V _{NO} or V _{NC} = 1.0V, 4.5V, or floating	Full			1.5	μA
PWROFF Leakage Current	I _{COM(PWROFF)}	V ₊ = 0V, V _{NO} or V _{NC} = 0V, V _{COM} = 5.0V	Full			10	μA
DIGITAL INPUTS							
Input High Voltage	V _{INH}		Full	1.65			V
Input Low Voltage	V _{INL}		Full			0.6	V
Input Leakage Current	I _{IN}	V ₊ = 5.0V, V _{IN} = 0V or 5.0V	Full			1.5	μA
DYNAMIC CHARACTERISTICS							
Turn-On Time	t _{ON}	V _{NO} or V _{NC} = 3.0V, R _L = 300Ω, C _L = 35pF, Test Circuit 2	+25°C		15		ns
Turn-Off Time	t _{OFF}	V _{NO} or V _{NC} = 3.0V, R _L = 300Ω, C _L = 35pF, Test Circuit 2	+25°C		11		ns
Break-Before-Make Time Delay	t _D	V _{NO} or V _{NC} = 3.0V, R _L = 300Ω, C _L = 35pF, Test Circuit 3	+25°C		6		ns
Skew	t _{SKEW}	R _S = 39Ω, C _L = 50pF, Test Circuit 4	+25°C		1		ns
Off Isolation	O _{ISO}	Signal = 0dBm, R _L = 50Ω, C _L = 5pF, Test Circuit 5	f = 10MHz	+25°C	-49		dB
			f = 1MHz	+25°C	-70		dB
-3dB Bandwidth	BW	Signal = 0dBm, R _L = 50Ω, C _L = 5pF, Test Circuit 6	+25°C		400		MHz
Charge Injection	Q	V _G = GND, R _G = 0Ω, C _L = 1.0nF, Test Circuit 7	+25°C		16		pC
Switch ON Capacitance	C _{ON}		+25°C		24		pF
Switch OFF Capacitance	C _{OFF}		+25°C		9		pF
POWER REQUIREMENTS							
Power Supply Current	I ₊	V ₊ = 5.0V, V _{IN} = 0V or V ₊	+25°C		0.5	8	μA

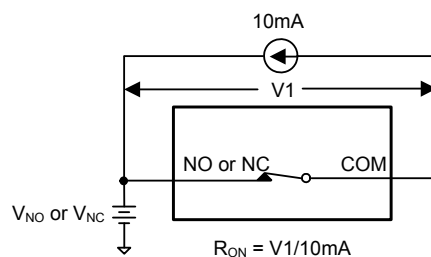
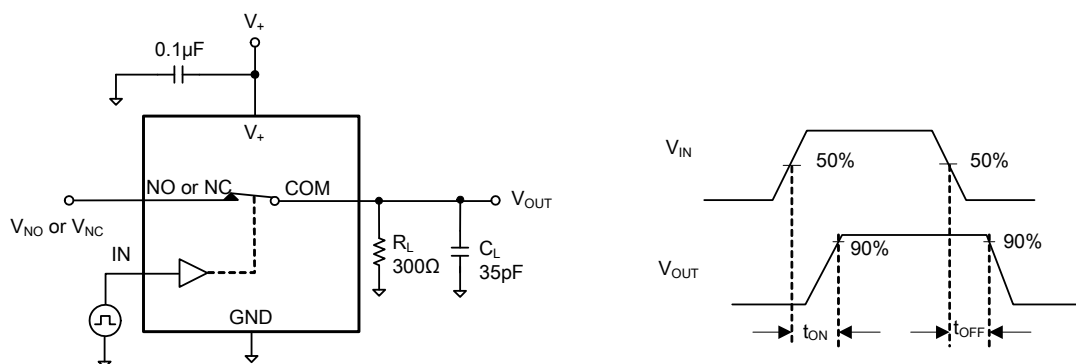
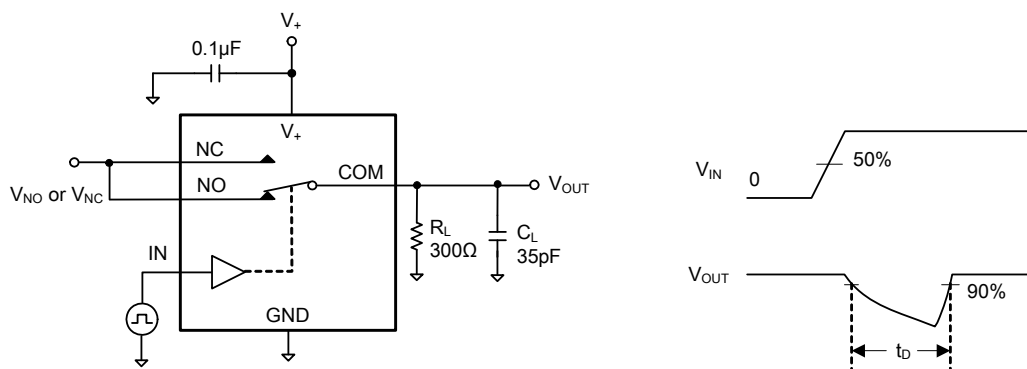
ELECTRICAL CHARACTERISTICS

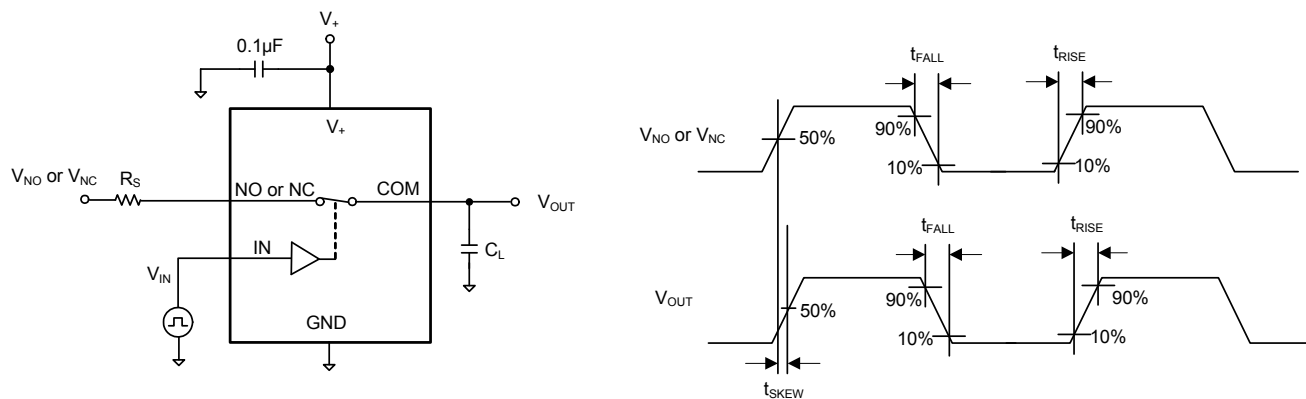
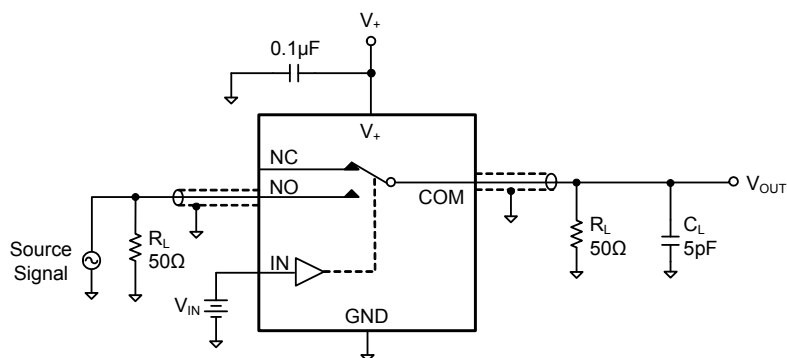
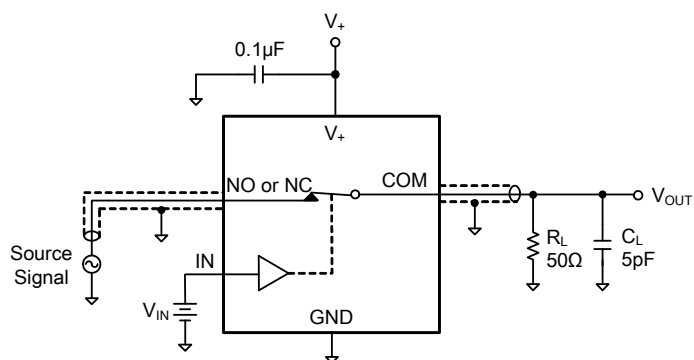
($V_+ = +2.7V$ to $+3.6V$, Full = $-40^\circ C$ to $+85^\circ C$, typical values are at $V_+ = 3.0V$, $T_A = +25^\circ C$, unless otherwise noted.)

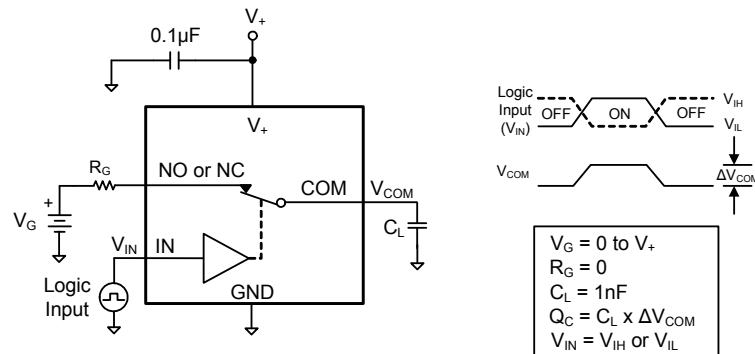
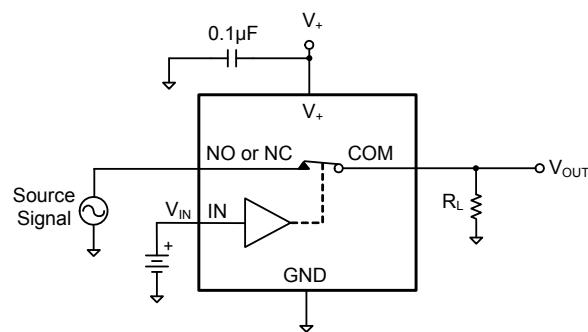
PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V_{NO}, V_{NC}, V_{COM}	$2.5V \leq V_+ \leq 3.5V$	Full	-2		V_+	V
		$3.5V \leq V_+ \leq 5.0V$		(V_+) - 5.5	V_+		
On-Resistance	R_{ON}	$V_+ = 2.7V, 0V \leq V_{NO} \text{ or } V_{NC} \leq V_+,$ $I_{COM} = -10mA$, Test Circuit 1	+25°C		8	11	Ω
			Full			12.2	
On-Resistance Match Between Channels	ΔR_{ON}	$V_+ = 2.7V, 0V \leq V_{NO} \text{ or } V_{NC} \leq V_+,$ $I_{COM} = -10mA$, Test Circuit 1	+25°C		0.1	0.65	Ω
			Full			0.7	
On-Resistance Flatness	$R_{FLAT(ON)}$	$V_+ = 2.7V, 0V \leq V_{NO} \text{ or } V_{NC} \leq V_+,$ $I_{COM} = -10mA$, Test Circuit 1	+25°C		3.5	6	Ω
			Full			6.8	
Source OFF Leakage Current	$I_{NC(OFF)}, I_{NO(OFF)}$	$V_+ = 3.6V, V_{NO} \text{ or } V_{NC} = 0.3V, 3.3V,$ $V_{COM} = 3.3V, 0.3V$	Full			1	μA
Channel ON Leakage Current	$I_{NC(ON)}, I_{NO(ON)},$ $I_{COM(ON)}$	$V_+ = 3.6V, V_{COM} = 0.3V, 3.3V,$ $V_{NO} \text{ or } V_{NC} = 0.3V, 3.3V$, or floating	Full			1.5	μA
PWROFF Leakage Current	$I_{COM(PWROFF)}$	$V_+ = 0V, V_{NO} \text{ or } V_{NC} = 0V, V_{COM} = 3.0V$	Full			10	μA
DIGITAL INPUTS							
Input High Voltage	V_{INH}		Full	1.55			V
Input Low Voltage	V_{INL}		Full			0.45	V
Input Leakage Current	I_{IN}	$V_+ = 3.6V, V_{IN} = 0V \text{ or } 3.6V$	Full			1.5	μA
DYNAMIC CHARACTERISTICS							
Turn-On Time	t_{ON}	$V_{NO} \text{ or } V_{NC} = 1.5V, R_L = 300\Omega,$ $C_L = 35pF$, Test Circuit 2	+25°C		24		ns
Turn-Off Time	t_{OFF}	$V_{NO} \text{ or } V_{NC} = 1.5V, R_L = 300\Omega,$ $C_L = 35pF$, Test Circuit 2	+25°C		15		ns
Break-Before-Make Time Delay	t_D	$V_{NO} \text{ or } V_{NC} = 1.5V, R_L = 300\Omega,$ $C_L = 35pF$, Test Circuit 3	+25°C		10		ns
Skew	t_{SKEW}	$R_S = 39\Omega, C_L = 50pF$, Test Circuit 4	+25°C		1		ns
Off Isolation	O_{ISO}	Signal = 0dBm, $R_L = 50\Omega, C_L = 5pF$, Test Circuit 5	f = 10MHz	+25°C	-49		dB
			f = 1MHz	+25°C	-70		dB
-3dB Bandwidth	BW	Signal = 0dBm, $R_L = 50\Omega,$ $C_L = 5pF$, Test Circuit 6	+25°C		400		MHz
Charge Injection	Q	$V_G = GND, R_G = 0\Omega,$ $C_L = 1.0nF$, Test Circuit 7	+25°C		10		pC
Switch ON Capacitance	C_{ON}		+25°C		24		pF
Switch OFF Capacitance	C_{OFF}		+25°C		9		pF
Total Harmonic Distortion	THD	$V_+ = 3.3V, V_{NC/NO} = 2.0V_{PP}, R_L = 600\Omega$ f = 20Hz to 20kHz, Test Circuit 8	+25°C		0.025		%
		$V_+ = 3.3V, V_{NC/NO} = 1.0V_{PP}, R_L = 32\Omega$ f = 20Hz to 20kHz, Test Circuit 8	+25°C		0.35		
POWER REQUIREMENTS							
Power Supply Current	I_+	$V_+ = 3.6V, V_{IN} = 0V \text{ or } V_+$	+25°C		0.5	5	μA

TYPICAL PERFORMANCE CHARACTERISTICS



TEST CIRCUITS

Test Circuit 1. On-Resistance

Test Circuit 2. Switching Times

Test Circuit 3. Break-Before-Make Time Delay (t_b)

TEST CIRCUITS (Cont.)

Test Circuit 4. Output Signal Skew (t_{SKEW})

Test Circuit 5. Off Isolation (O_{ISO})

Test Circuit 6. -3dB Bandwidth (BW)

TEST CIRCUITS (Cont.)

Test Circuit 7. Charge Injection (Q)

Test Circuit 8. Total Harmonic Distortion (THD)

APPLICATION

In order to enhance the negative signal swing capability of SGM3719, the circuit in Figure 1 is recommended. R1 and R2 will prevent the device from entering into latch-up state when passing negative signal.

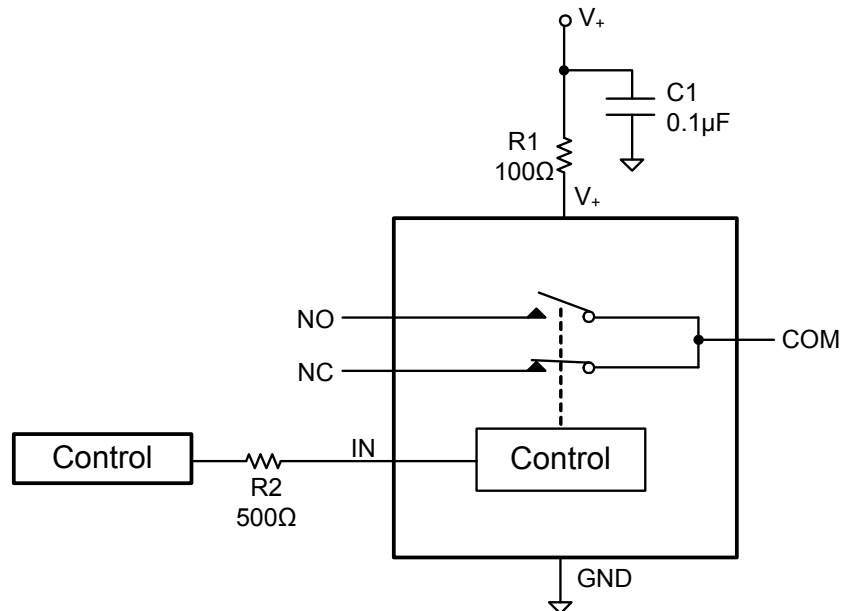
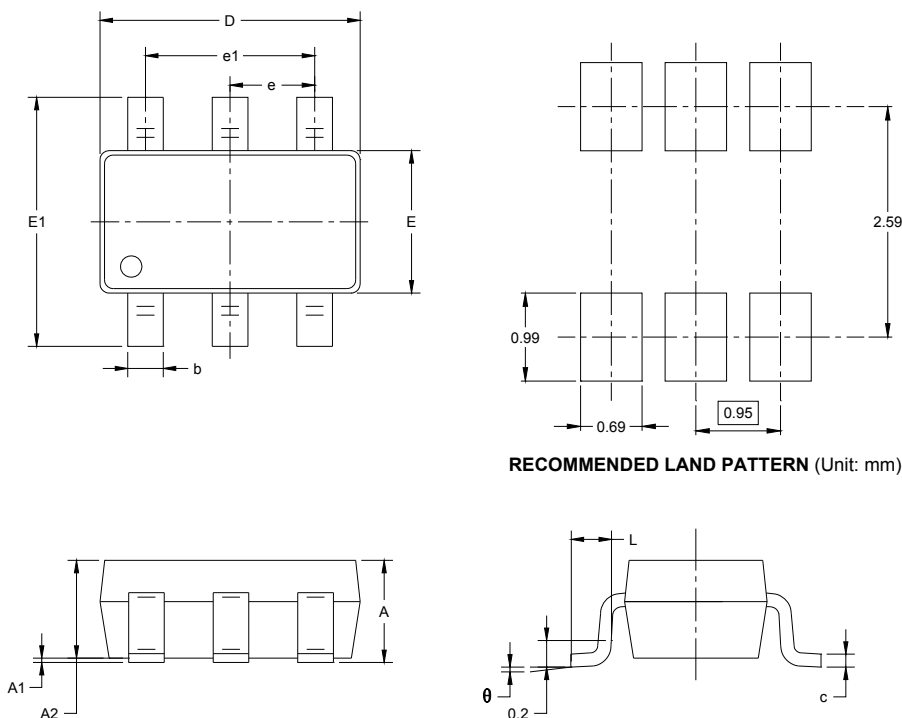


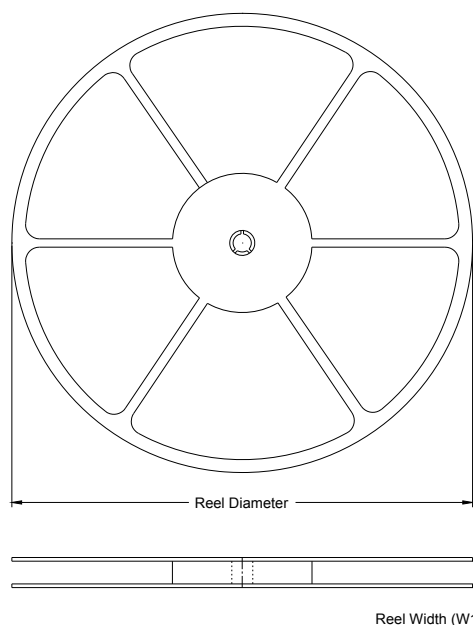
Figure 1. Typical Application Circuit

PACKAGE OUTLINE DIMENSIONS
SOT-23-6


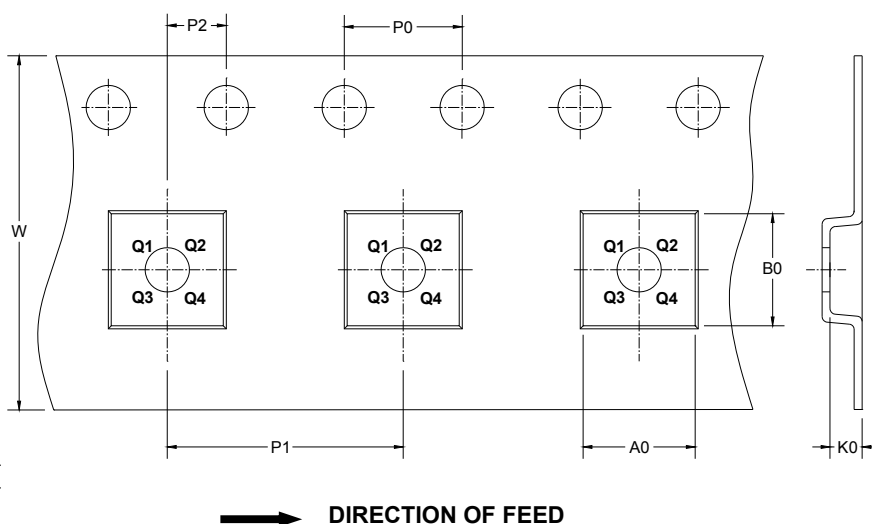
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950 BSC		0.037 BSC	
e1	1.900 BSC		0.075 BSC	
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

TAPE AND REEL INFORMATION

REEL DIMENSIONS



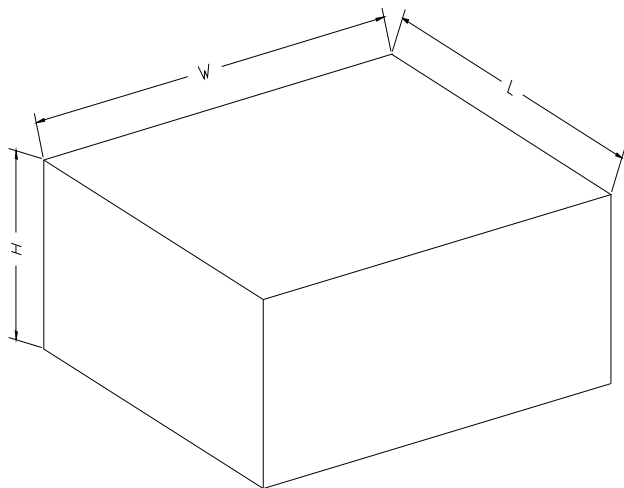
TAPE DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOT-23-6	7"	9.5	3.17	3.23	1.37	4.0	4.0	2.0	8.0	Q3

CARTON BOX DIMENSIONS

NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7" (Option)	368	227	224	8
7"	442	410	224	18