

SGM5223

0.5Ω Ultra Low ON-Resistance, Dual, SPDT Analog Switch

GENERAL DESCRIPTION

The SGM5223 is a dual single-pole/double-throw (SPDT) analog switch designed to operate from a single +1.8V to +4.2V power supply. Targeted applications include battery powered equipment that benefit from SGM5223's low ON-resistance (0.5Ω) and fast switching speeds ($t_{ON} = 17\text{ns}$, $t_{OFF} = 27.5\text{ns}$).

SGM5223 has excellent on-resistance matching (0.18 Ω max) between switches and guarantees excellent on-resistance flatness over all signal range (0.1Ω TYP). This ensures excellent linearity and low distortion when switching audio signals.

The SGM5223 is a committed dual single-pole/double-throw (SPDT) that consist of two normally open (NO) and two normally close (NC) switches. This configuration can be used as a dual 2-to-1 multiplexer.

SGM5223 is available in Pb-free WQFN-10 package.

APPLICATIONS

Portable Instrumentation
Battery-Operated Equipment
Computer Peripherals
Speaker and Earphone Switching
Medical Equipment
Audio and Video Switching

FUNCTION TABLE

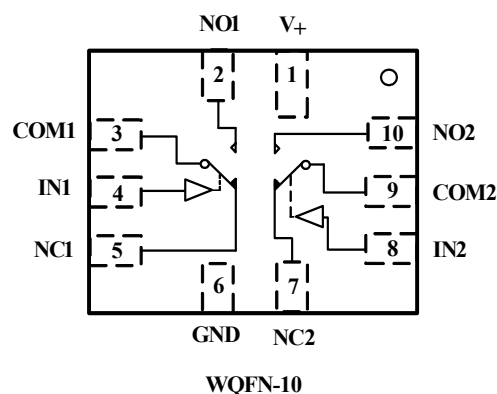
LOGIC	NO	NC
0	OFF	ON
1	ON	OFF

Switches Shown For Logic "0" Input

FEATURES

- Voltage Operation : 1.8V to 4.2V
- Ultra Low On-Resistance: 0.5Ω (TYP) at 4.2V
- Fast Switching Time
 - $t_{ON} 17\text{ ns}$
 - $t_{OFF} 27.5\text{ ns}$
- High Off-Isolation: -58dB at 1MHz
- Low Crosstalk: -104dB at 1MHz
- Rail-to-Rail input and output Operation
- 1.8V logic Compatible control pin
- Break-Before-Make Switching
- Extended Industrial Temperature Range:
 - 40°C to 85°C
- Lead (Pb) Free WQFN-10 Package

PIN CONFIGURATION (TOP VIEW)



ELECTRICAL CHARACTERISTICS

(V₊ = +4.2V, GND = 0V, V_{IH} = +1.6V, V_{IL} = +0.6V, T_A = - 40°C to + 85°C. Typical values are at V₊ = +4.2V, T_A = + 25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TPY	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V _{NO} , V _{NC} , V _{COM}		- 40°C to +85°C	0		V ₊	V
On-Resistance	R _{ON}	V ₊ = 4.2 V, V _{NO} or V _{NC} = 1V, I _{COM} = -100 mA, Test Circuit 1	+25°C		0.5	0.75	Ω
			- 40°C to +85°C			0.85	Ω
On-Resistance Match Between Channels	ΔR _{ON}	V ₊ = 4.2V, V _{NO} or V _{NC} = 1V, I _{COM} = -100 mA, Test Circuit 1	+25°C		0.11	0.18	Ω
			- 40°C to +85°C		0.1	0.23	Ω
On-Resistance Flatness	R _{FLAT(ON)}	V ₊ = 4.2V, V _{NO} or V _{NC} = 1V, I _{COM} = -100 mA, Test Circuit 1	+25°C		0.1	0.22	Ω
			- 40°C to +85°C			0.26	Ω
Source OFF Leakage current	I _{NC(OFF)} , I _{NO(OFF)}	V ₊ = 4.2V, V _{NO} or V _{NC} = 3.3V/ 0.3V, V _{COM} = 0.3V/ 3.3V	- 40°C to +85°C			1	μA
Channel ON Leakage current	I _{NC(ON)} , I _{NO(ON)} , I _{COM(ON)}	V ₊ = 4.2V, V _{COM} = 0.3V/ 3.3V, V _{NO} or V _{NC} = 0.3V/ 3.3V, or floating	- 40°C to +85°C			1	μA
DIGITAL INPUTS							
Input High Voltage	V _{IINH}		- 40°C to +85°C	1.6			V
Input Low Voltage	V _{INL}		- 40°C to +85°C			0.5	V
Input Leakage Current	I _{IN}	V ₊ = 4.2V, V _{IN} = 0 or 4.2V	- 40°C to +85°C			1	μA
DYNAMIC CHARACTERISTICS							
Turn-On Time	t _{ON}	V _{IH} = 3V, V _{IL} = 0V, Test Circuit2	+25°C		17.0		ns
Turn-Off Time	t _{OFF}	V _{IH} = 3V, V _{IL} = 0V, Test Circuit2	+25°C		27.5		ns
Break-Before-Make Time Delay	t _d	V _{IH} = 3V, V _{IL} = 0V, Test Circuit3	+25°C		5.0		ns
Off Isolation	O _{ISO}	V _{BIAS} = 2.1 V, V _{IN} = 0dBm V _{IH} = 3V, V _{IL} = 0V, Test Circuit4	100KHz	+25°C		-78	dB
			1MHz	+25°C		-58	dB
Channel-to-Channel Crosstalk	X _{TALK}	V _{BIAS} = 2.1 V, V _{IN} = 0dBm V _{IH} = 3V, V _{IL} = 0V, Test Circuit5	100KHz	+25°C		-100	dB
			1MHz	+25°C		-104	dB
Bandwidth -3 dB	BW	V _{BIAS} = 2.1 V, V _{IN} = 0 dBm, V _{IH} = 3V, V _{IL} = 0V, Test Circuit6	+25°C		55		MHz
Channel ON Capacitance	C _{ON}		+25°C		95		pF
Charge Injection Select Input to Common I/O	Q	V _{IN} = GND, R _S = 0 Ω, C _L = 1.0 nF, Q = C _L × V _{OUT} , V _{IH} = 3V, V _{IL} = 0V, Test Circuit7	+25°C		13		pC
POWER REQUIREMENTS							
Power Supply Range	V ₊		- 40°C to +85°C	1.8		4.2	V
Power Supply Current	I ₊	V ₊ = 4.2V, V _{IN} = 0V or V ₊	- 40°C to +85°C			1	μA

Specifications subject to change without notice.

ELECTRICAL CHARACTERISTICS

(V₊ = +2.7V to +3.6V, V_{IH} = +1.6V, V_{IL} = +0.4V, T_A = - 40°C to +85°C. Typical values are at V₊ = +3.0V, T_A = + 25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TPY	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V _{NO} , V _{NC} , V _{COM}		- 40°C to +85°C	0		V ₊	V
On-Resistance	R _{ON}	V ₊ = 2.7 V, V _{NO} or V _{NC} = 1V, I _{COM} = -100 mA, Test Circuit 1	+25°C		0.6	0.9	Ω
			- 40°C to +85°C			1	Ω
On-Resistance Match Between Channels	ΔR _{ON}	V ₊ = 2.7V, V _{NO} or V _{NC} = 1V, I _{COM} = -100 mA, Test Circuit 1	+25°C		0.15	0.22	Ω
			- 40°C to +85°C		0.15	0.25	Ω
On-Resistance Flatness	R _{FLAT(ON)}	V ₊ = 2.7V, V _{NO} or V _{NC} = 1V, I _{COM} = -100 mA, Test Circuit 1	+25°C		0.1	0.22	Ω
			- 40°C to +85°C		0.1	0.26	Ω
Source OFF Leakage current	I _{NC(OFF)} , I _{NO(OFF)}	V ₊ = 3.6V, V _{NO} or V _{NC} = 3.3V / 0.3V, V _{COM} = 0.3V/ 3.3V	- 40°C to +85°C			1	μA
Channel ON Leakage current	I _{NC(ON)} , I _{NO(ON)} , I _{COM(ON)}	V ₊ = 3.6V, V _{COM} = 0.3V/ 3.3V, V _{NO} or V _{NC} = 0.3V/ 3.3V, or floating	- 40°C to +85°C			1	μA
DIGITAL INPUTS							
Input High Voltage	V _{IINH}		- 40°C to +85°C	1.5			V
Input Low Voltage	V _{INL}		- 40°C to +85°C			0.4	V
Input Leakage Current	I _{IN}	V ₊ = 2.7V, V _{IN} = 0 or 2.7V	- 40°C to +85°C			1	μA
DYNAMIC CHARACTERISTICS							
Turn-On Time	t _{ON}	V _{IH} = 1.5V, V _{IL} = 0V, Test Circuit2	+25°C		22		ns
Turn-Off Time	t _{OFF}	V _{IH} = 1.5V, V _{IL} = 0V, Test Circuit2	+25°C		34		ns
Break-Before-Make Time Delay	t _D	V _{IH} = 1.5V, V _{IL} = 0V, Test Circuit3	+25°C		15.5		ns
Off Isolation	O _{ISO}	V _{BIAS} = 1.5 V, V _{IN} = 0 dBm, V _{IH} = 1.5V, V _{IL} = 0V, Test Circuit4	100KHz	+25°C		-78	dB
			1MHz	+25°C		-58	dB
Channel-to-Channel Crosstalk	X _{TALK}	V _{BIAS} = 1.5 V, V _{IN} = 0 dBm, V _{IH} = 1.5V, V _{IL} = 0V, Test Circuit5	100KHz	+25°C		-100	dB
			1MHz	+25°C		-104	dB
Bandwidth -3 dB	BW	V _{BIAS} = 1.5 V, V _{IN} = 0 dBm, V _{IH} = 1.5V, V _{IL} = 0V, Test Circuit6	+25°C		55		MHz
Charge Injection Select Input to Common I/O	Q	V _{IN} = GND, R _S = 0 Ω, C _L = 1.0 nF, Q = C _L × V _{OUT} , V _{IH} = 1.5V, V _{IL} = 0V, Test Circuit7	+25°C		8.5		pC
Channel ON Capacitance	C _{ON}		+25°C		95		pF

Specifications subject to change without notice.

ORDERING INFORMATION

MODEL	PIN-PACKAGE	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKAGE OPTION
SGM5223	WQFN-10	- 40°C to +85°C	SGM5223YWQ10/TR	5223	Tape and Reel, 3000

ABSOLUTE MAXIMUM RATINGS

V ₊ , IN to GND.....	0V to +4.6V	Junction Temperature.....	+150°C
Analog, Digital voltage range(1).....	- 0.3V to (V ₊ + 0.3V)	Storage Temperature.....	- 65°C to +150°C
Continuous Current NO, NC, or COM.....	±250mA	Lead Temperature (soldering, 10s).....	+260°C
Peak Current NO, NC, or COM.....	±350mA	ESD (HBM).....	4000V
Operating Temperature Range.....	- 40°C to +85°C		

Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

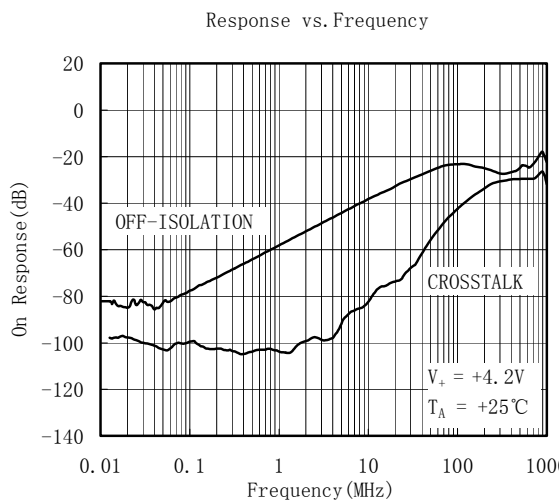
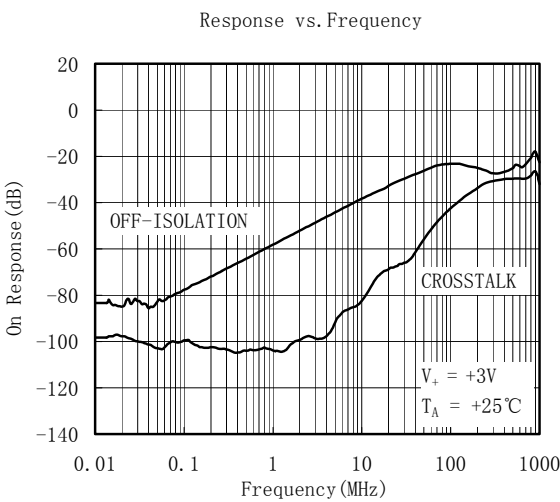
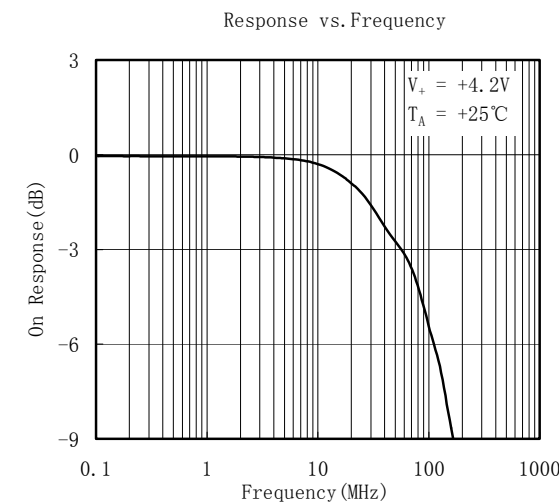
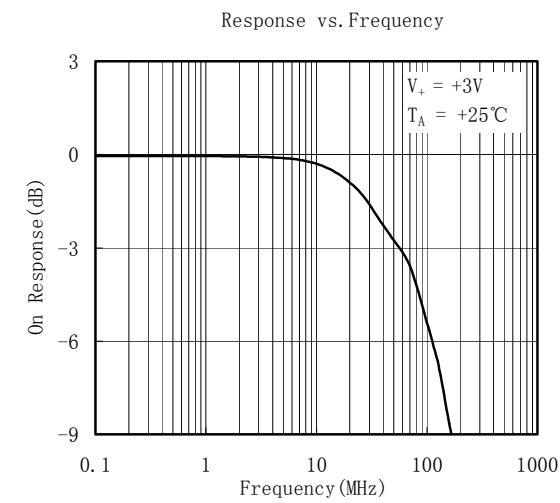
(1) Signals on NC, NO, or COM or IN exceeding V₊ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.

PIN DESCRIPTION

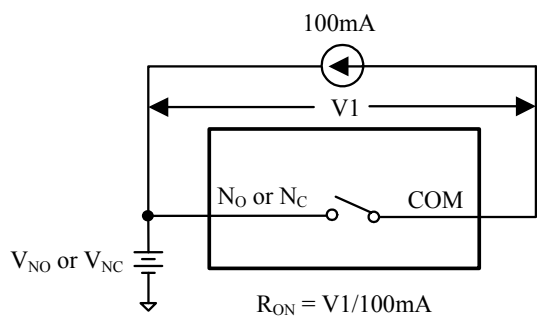
WQFN PIN	NAME	FUNCTION
1	V ₊	Power supply
6	GND	Ground
4,8	IN1, IN2	Digital control pin to connect the COM terminal to the NO or NC terminals
3,9	COM1, COM2	Common terminal
2,10	NO1, NO2	Normally-open terminal
5,7	NC1, NC2	Normally-closed terminal

Note: NO, NC and COM terminal may be an input or output.

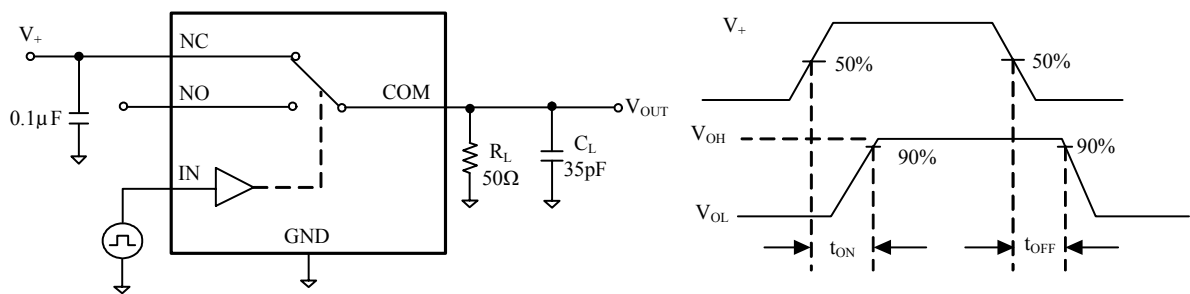
TYPICAL PERFORMANCE CHARACTERISTICS



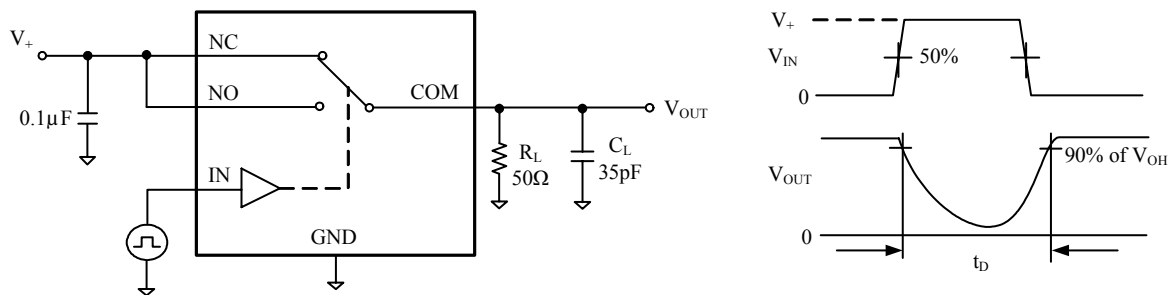
TEST CIRCUITS



Test Circuit 1. On Resistance

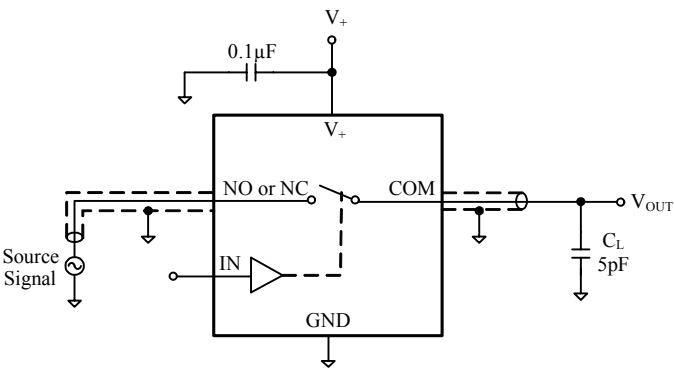


Test Circuit 2. Switching Times (t_{ON}, t_{OFF})

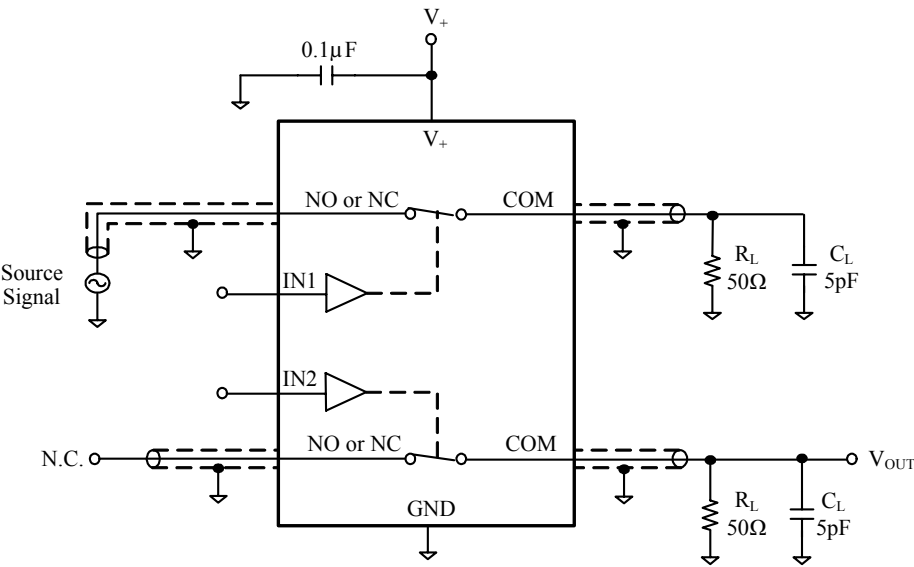


Test Circuit 3. Break-Before-Make Time (t_d)

TEST CIRCUITS (Cont.)



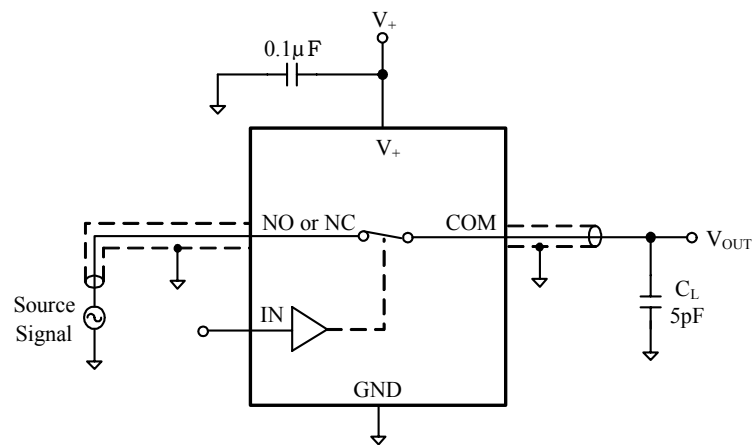
Test Circuit 4. Off Isolation



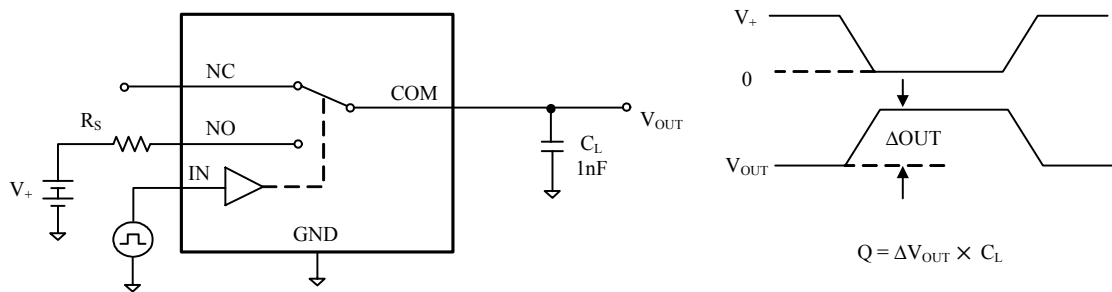
$$\text{Channel To Channel Crosstalk} = -20 \times \log \frac{V_{\text{NO or V}_{\text{NC}}}}{V_{\text{OUT}}}$$

Test Circuit 5. Channel-to-Channel Crosstalk

TEST CIRCUITS (Cont.)



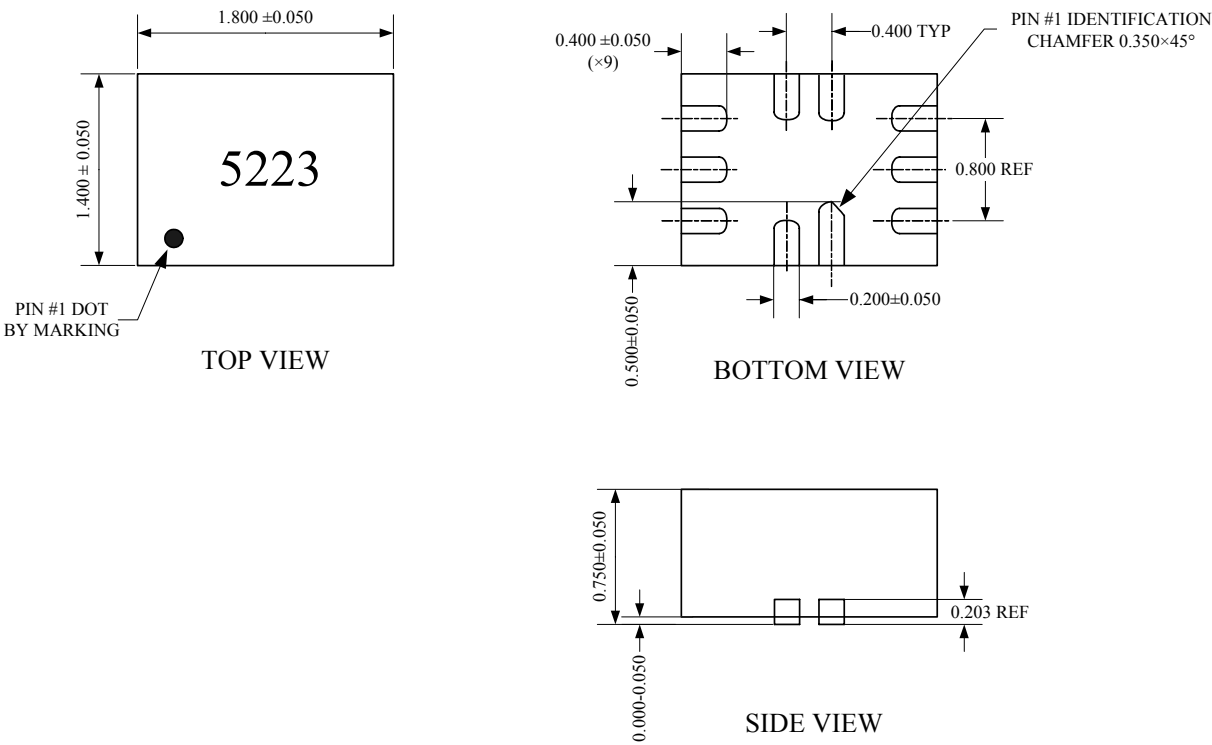
Test Circuit 6. Bandwidth



Test Circuit 7. Charge Injection (Q)

PACKAGE OUTLINE DIMENSIONS

WQFN-10



Note: All linear dimensions are in millimeters.

REVISION HISTORY

Location

Page

10/07— Data Sheet REV.A

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